

# ECE 562

Exam 1 review

Fall 2008

# Exam 1 review

## Summary

- Section notes
  - Slides 3-14 -Considerations in choosing power devices
  - Slides 15-23 – Power device market trends
  - Slides 24-28– Buck-Boost review
  - Slides 29-34 – Converter efficiency
  - Slides 35-42 – Commercial devices
  - Slides 42-44– Review
  - Slides 45-50 – Synchronous converters
  - Slides 51-61 – Buck-Boost review
  - Slides 62-74 – Noise, EMC/EMI and overview

# Architectural Trade-Offs: When do you use--

- A linear solution?
  - Linear
  - LDO
  - Controller
- An inductive-based Switcher?
  - Controller
  - Synchronous
  - Asynchronous
  - Multi-phase
- A switched capacitor converter?

# Architectural Trade-Offs: When do you use--

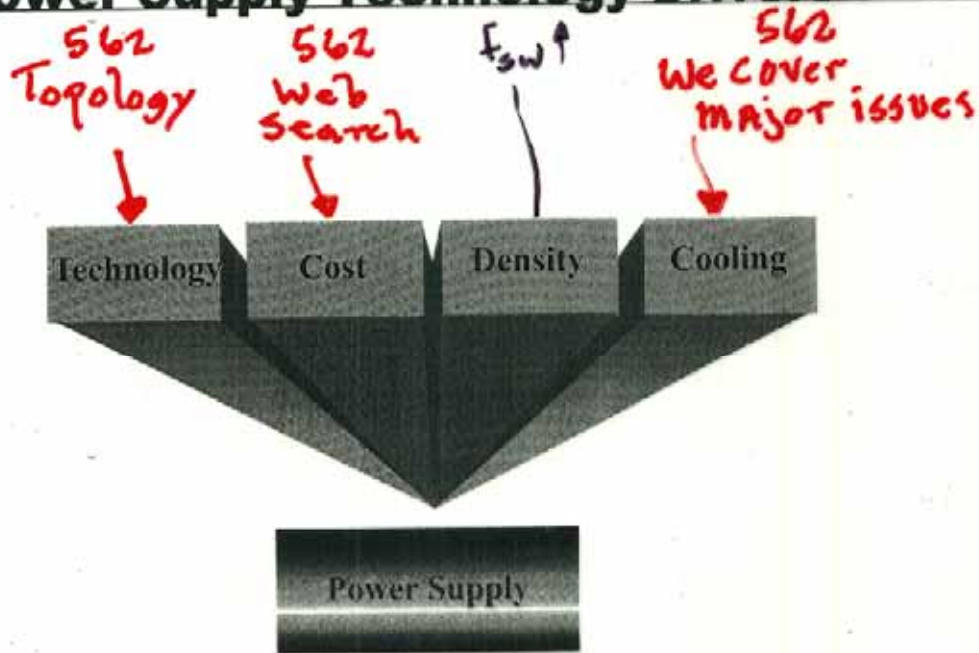
- **A linear solution?** ( $\eta < 70\%$ )
  - When simplicity and low-cost are key
  - When you have power to burn
    - Low currents
    - Low input V to output V differential
  - When you need noise isolation

# Architectural Trade-Offs:

## When do you use--

- **An inductive-based Switcher?**  
( $60\% < \eta < 97\%$ )
  - When conversion efficiency is needed
    - Heat concerns
    - Battery life
    - System reliability
  - For maximum power density where  $I > 1A$

# Power Supply Technology Drivers



# Applications

## • Line-Powered

- Computing systems
- Automotive
- Office automation
  - Printers
  - Scanners
  - Etc.
- Consumer appliances
  - Set-Top Box
  - Audio-Video systems
- Industrial applications
  - Process control
  - Data acquisition
  - Etc.

## • Battery-Powered

- Wireless
  - Mobile phones
  - Pagers
- PDAs
  - Palm Pilots
  - Palm Computers
- Cameras
- Computing
- Consumer appliances
  - Video recorders
  - MP3
  - DVD

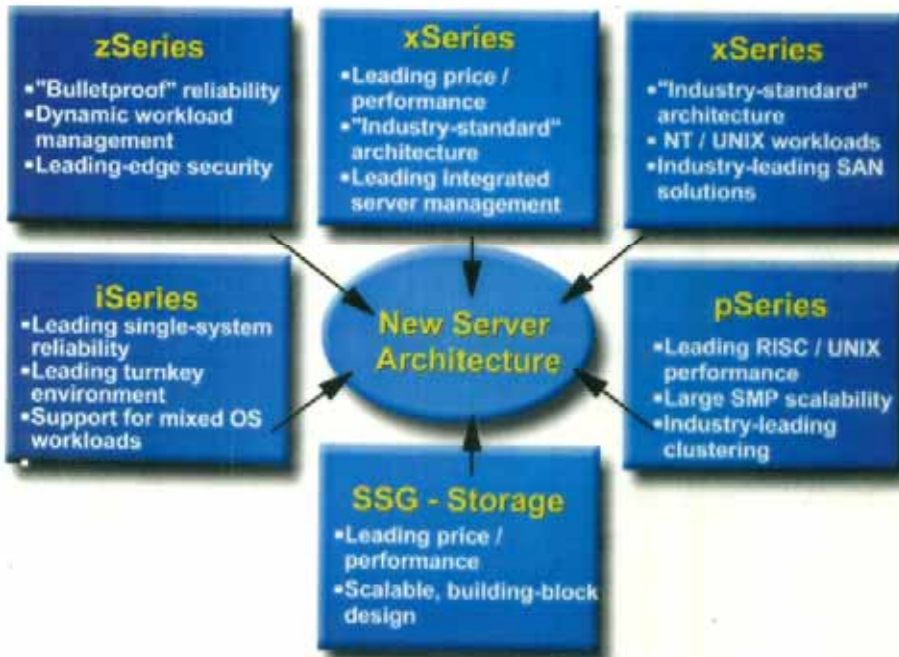
*different customers*

*National Semiconductor*

*unique demands*



# IBM Servers: Hardware Integration

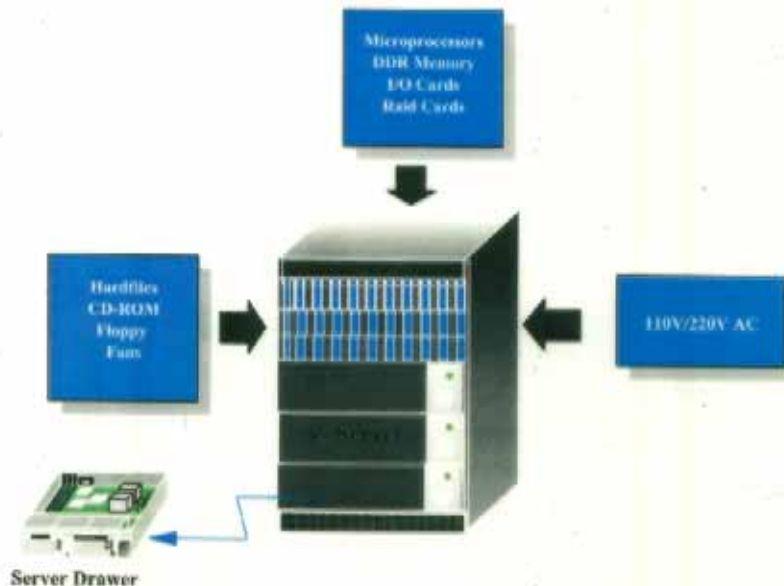


IBM





# Typical Server Application



IBM



# Power Supply Technology Drivers

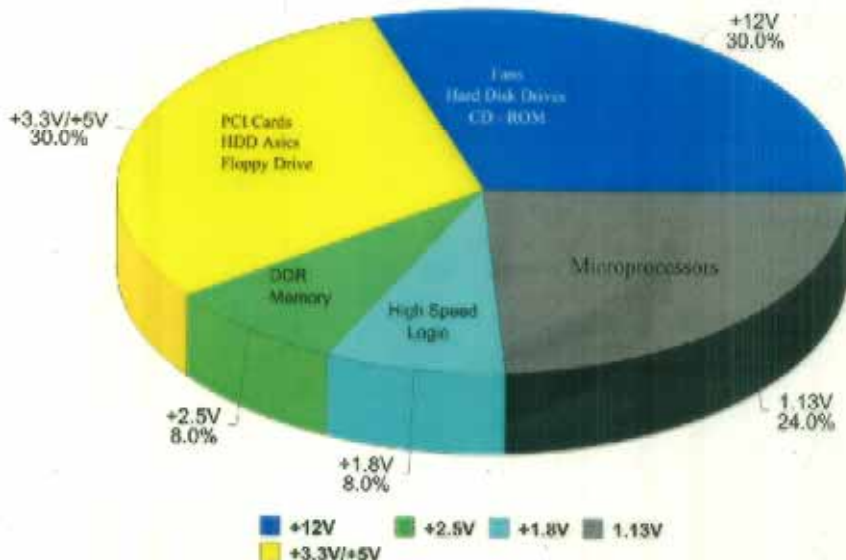


**IBM**



# System Power Distribution

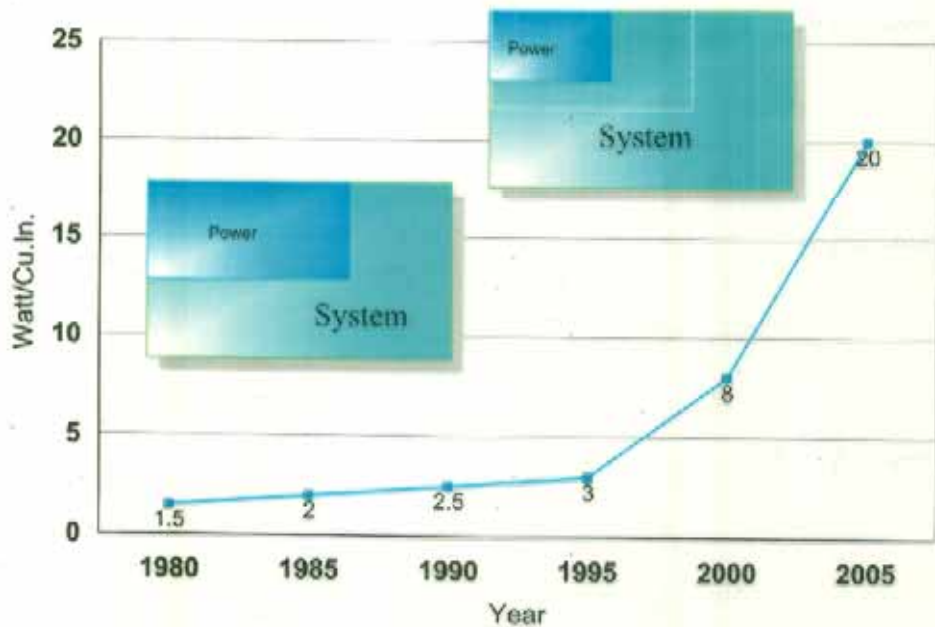
## Why 12V?



IBM



# Power Density Trend



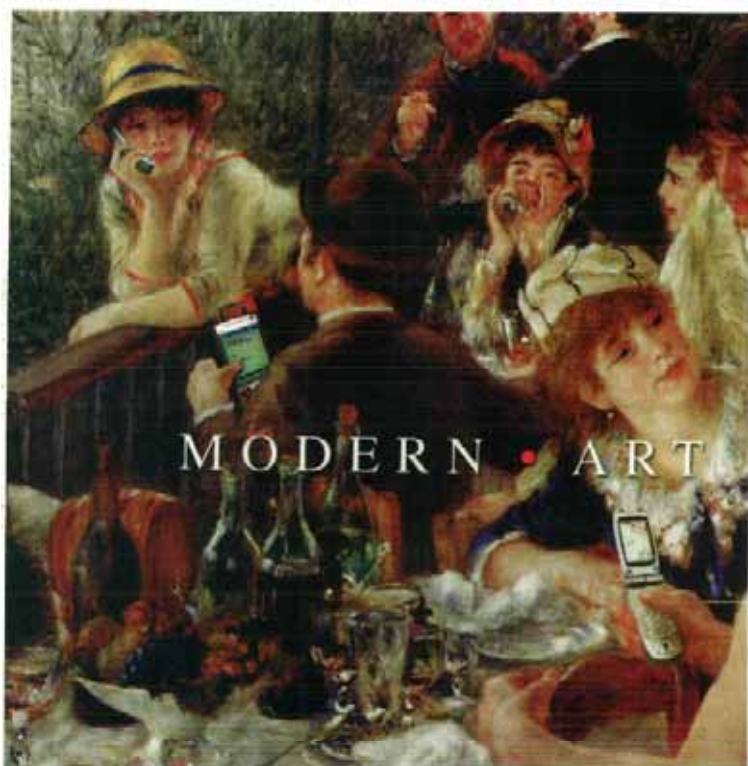
IBM



# Power Architectures

CLASS	ARCHITECTURES
Mobile	<ul style="list-style-type: none"><li>▪ Battery Technology Main Power Source</li><li>▪ Efficient DC/DC Converters &amp; Power Management</li></ul>
Low End	<ul style="list-style-type: none"><li>▪ Centralized Power Supply</li><li>▪ Point-Of-Load Regulator (uP or Memory)</li></ul>
Mid-Range	<ul style="list-style-type: none"><li>▪ Centralized Power Supply<ul style="list-style-type: none"><li>* Point-Of-Load Regulator (uP or Memory)</li><li>* DC/DC Regulators Imbedded in PS</li></ul></li><li>▪ Fully Distributed Power Supply<ul style="list-style-type: none"><li>* +12V Bus Voltage</li><li>* +12V Input DC/DC</li></ul></li><li>▪ Telco (-48V) Version</li></ul>
High End	<ul style="list-style-type: none"><li>▪ Fully Distributed Power Supply<ul style="list-style-type: none"><li>* +350V Bus Voltage</li><li>* +350V Input DC/DC</li></ul></li><li>▪ Fully Distributed Power Supply<ul style="list-style-type: none"><li>* +48V Bus Voltage</li><li>* +48V Input DC/DC</li></ul></li></ul>





MODERN • ART

# Power Management Market Drivers<sup>1</sup>

- **Growth Drivers**

- 1. Demand for higher efficiency portable electronics
- 2. 42V Automotive power
- 3. Need for greener products
- 4. Adoption of power management standards
- 5. Growth in telecom and computer applications

- **I.C. Technical/Feature Trends**

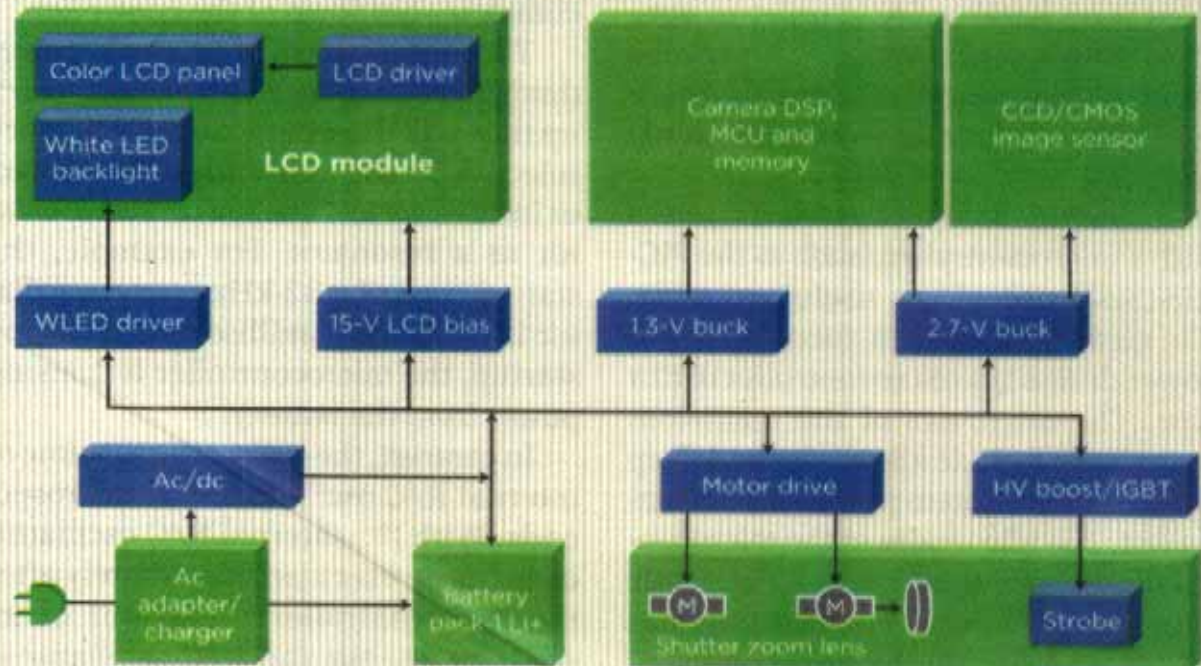
- 1. Lower quiescent current
- 2. Tighter tolerances
- 3. Switchers will replace some linear solutions
- 4. Integrated switchers will grow faster than controllers for <4A
- Increased use of synchronous rectification

<sup>1</sup> Venture Development Corp.

even "portable" draws power

## Digital still camera burns up power

Standalone consumption: 2 W per flash, 500 mW per LCD view





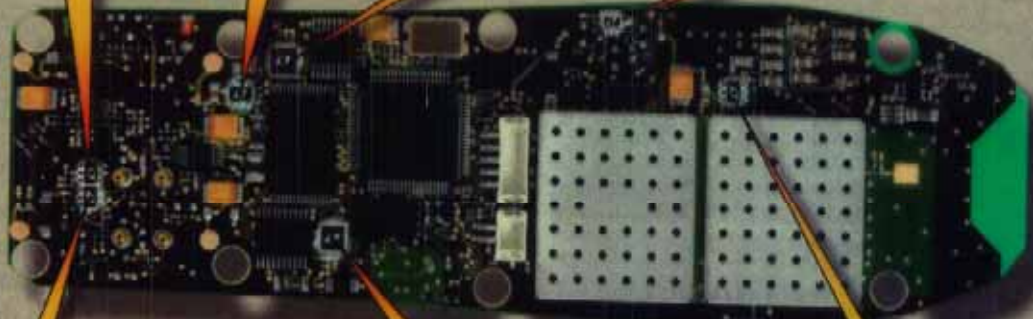
# Small Low Top Phones

Inductorless  
Boost Converter  
LTC3200-5

Step-Down  
DC/DC Converter  
LTC3406

Battery Charger  
LTC4058

Low Noise  
Boost Converter  
LT3460



USB Power  
Manager  
LTC4410

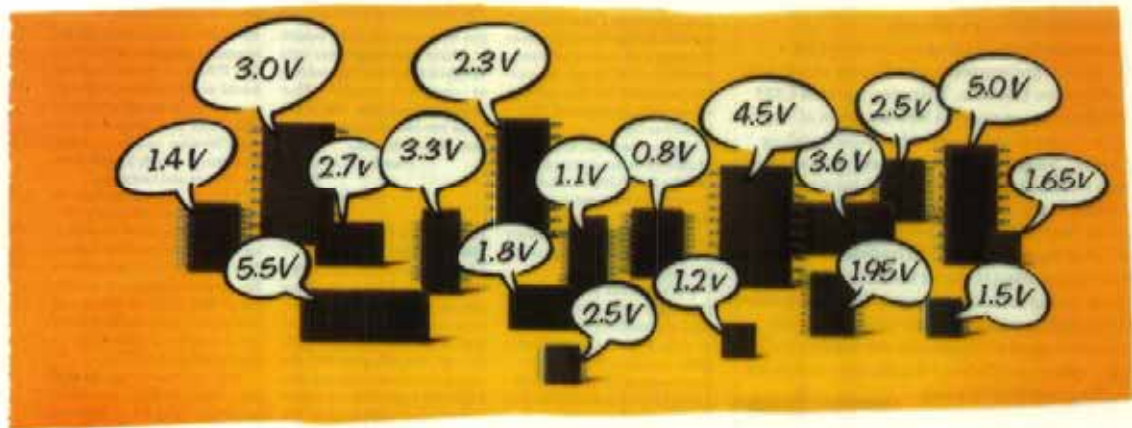
Buck-Boost  
Converter  
LTC3440/1

White LED Driver  
LT3465/A

Tiny & Efficient Power Solutions for Handheld Products

Ch 2  
3

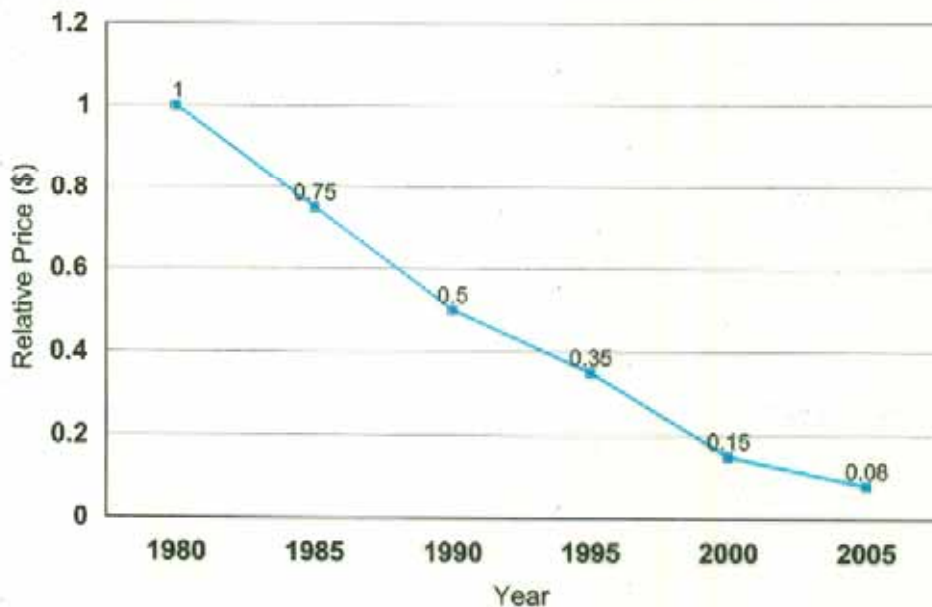
Ch 4





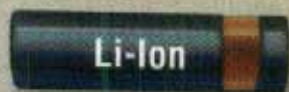
business

## Price Trend



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# 95% Efficient DC/DC Converters

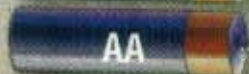


Li-Ion

or

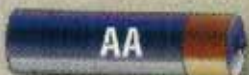


AA



AA

or



AA



**LTC3400**

95% Efficient ThinSOT  
Synchronous Boost

$V_{OUT} = 5V @ 300mA$



**LTC3440**

96% Efficient MSOP  
Buck/Boost

$V_{OUT} = 3.3V @ 600mA$



**LTC3405**

96% Efficient ThinSOT  
Synchronous Buck

$V_{OUT} = 1.8V @ 300mA$

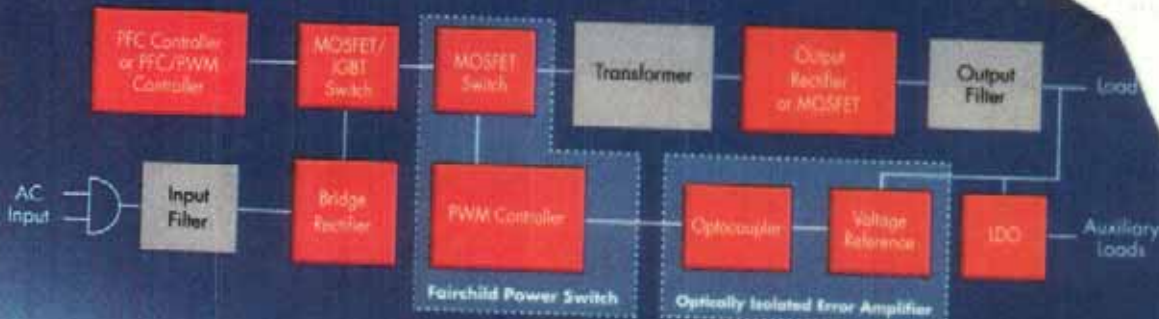
1mm Tall Power ICs Shrink Handheld Products

## Entry Level Engineer Expectations

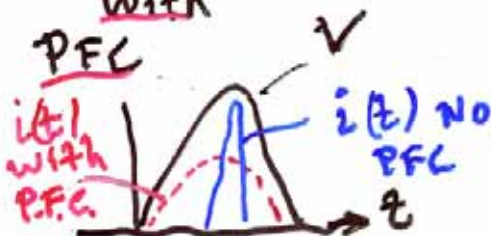
	Proficiency
✓ Switching power supply technology	4
✓ Analog circuit design and analysis techniques	3
✓ Understanding of magnetics	3
✓ Simulation skills	3
• Basic understanding	
✓ Communication skills	5
• Written	
• Oral	
✓ Teamwork experience and skills	5

*Talks*  
*Papers*

# Isolated AC/DC Power Supply



AC  $\rightarrow$  DC  
rectification  
with  
PFC

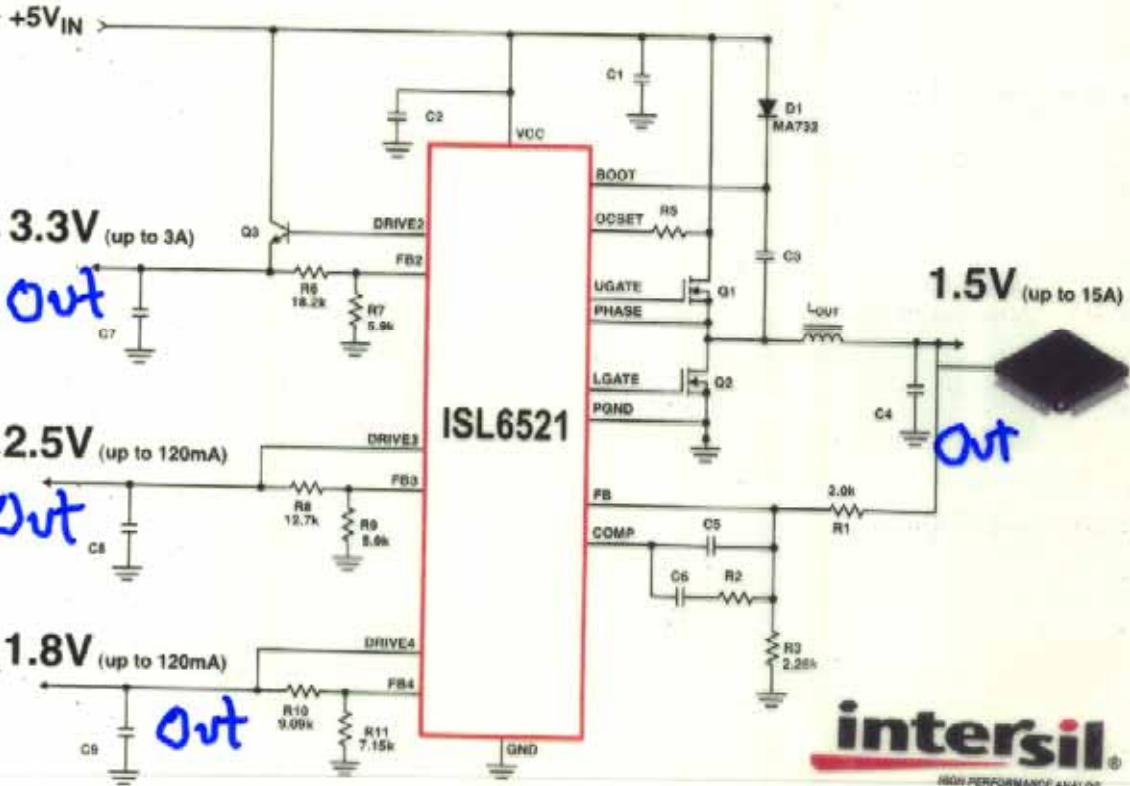


Inverter  
making  
D-waves

AC-DC  
PWM  
Rectifying via  
big L

resonant converter  
via rectifiers

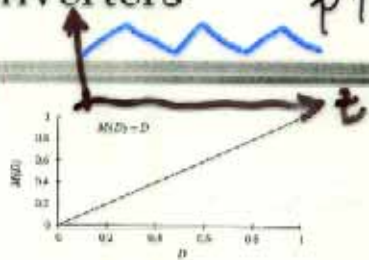
**IN**



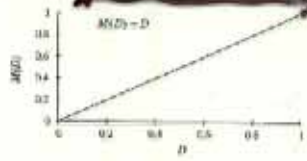
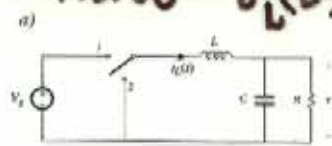
# Three basic dc-dc converters

Fig 2.5  
P16

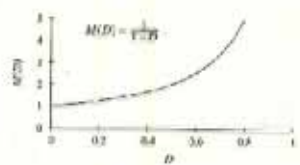
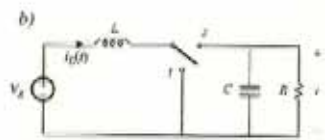
All have  $i_L(t)$



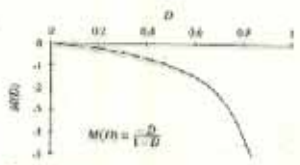
Buck  
down



Boost  
up



Buck-boost  
either way



Exam review

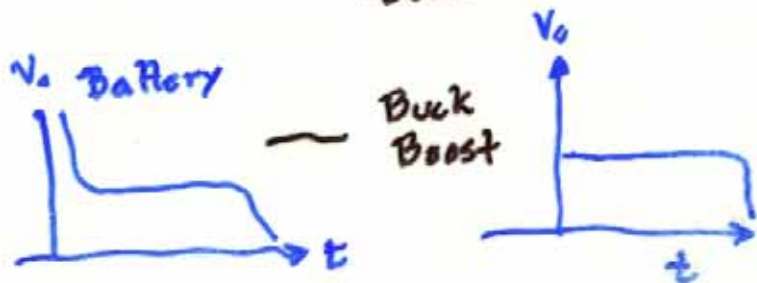
P6m 2.1

3.8



# Review

$V_{IN}$  (DC) — Buck  $V_o < V_{IN}$   
Boost  $V_o > V_{IN}$   
Buck-Boost

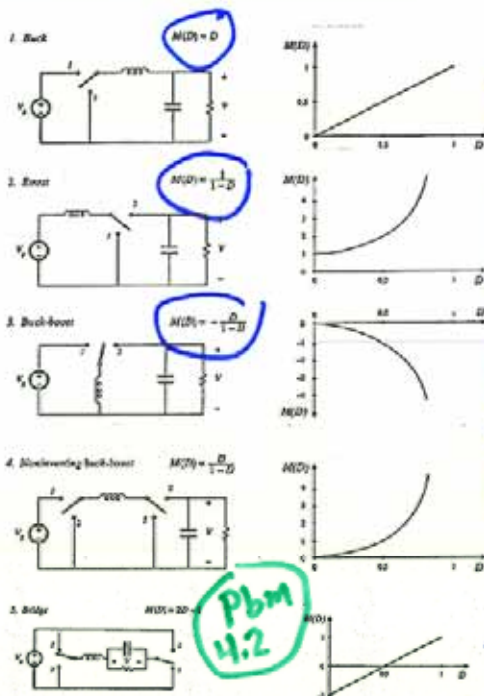


Linear Regulator (L.R.): LDO  
Use as  $\Delta V / \Delta V$  ripple killer

$V_{IN}$  — drop  $10\% V_{IN}$  —  $V_{out}$   $\eta = 90\%$

Buck drop  $90\% V_{IN}$  —  $V_{out}$   $\eta = 90\%$

- I. Summary - PWM single inductor converters
- A. Eight circuit topologies and DC transfer functions  $M(D)$  where  $M(D) = V_o/V_g = f(D)$ . Its crucial to emphasize that we assume LOSS LESS CIRCUITS BELOW.



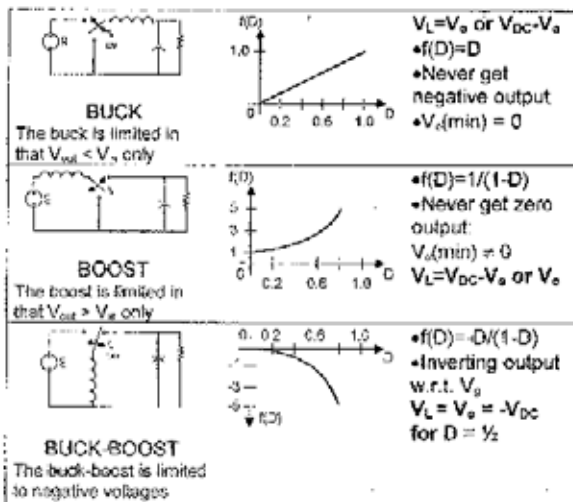
IC  
Chip  
Design

Pbm  
4.2

DC  
out  
vs  
D  
@ any  
fsw  
@ any  
I<sub>out</sub>

if  $D = \frac{1}{2} + D_m \sin \omega t$   
DC to AC Converter!

## SIMPLE SWITCH MODE CONVERTERS

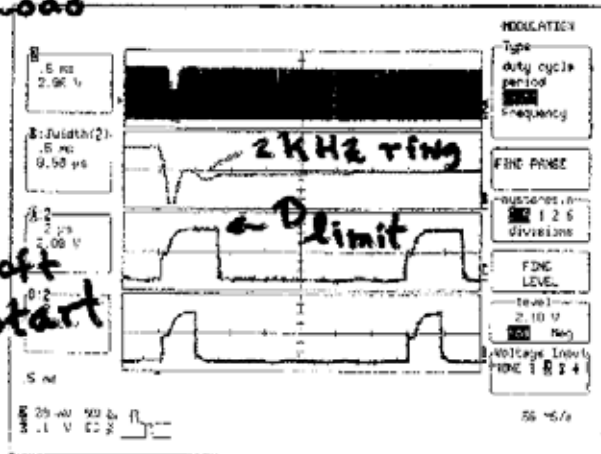


### 4. UNSYMMETRIC $i_L$ AND $v_C$ WAVEFORMS OF EQUAL INTEGRATED AREA IN THE ABOVE THREE CONVERTERS

# Transient load Performance D versus time

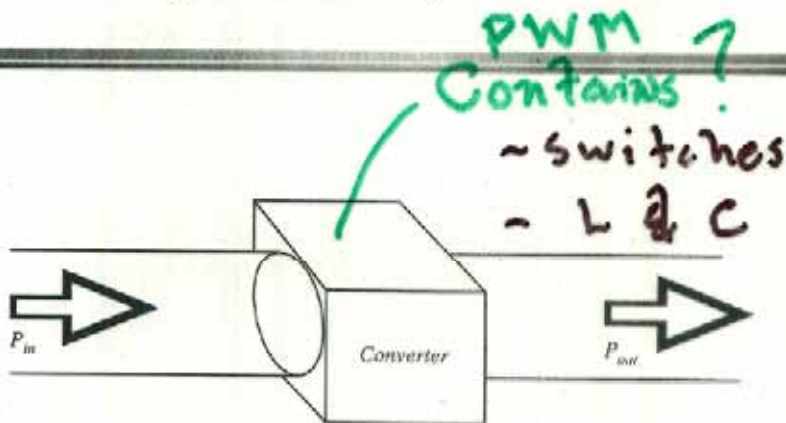
Change  
of load

soft  
start



As seen on a LeCroy 1T344L DSO, Trace 1 shows gate drive pulses over a 5-millisecond period that includes the transition from maximum to minimum load. Trace 2 displays Modulation Analysis that presents a record of each pulse width on the Y-axis. Traces 3 and 4 show individual gate drive pulses at full load and during recovery to minimum load, respectively.

## A high-efficiency converter

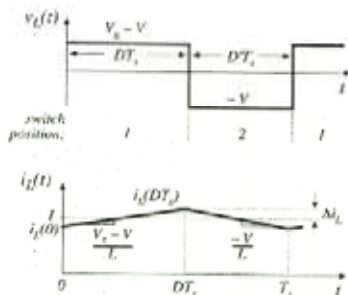


A goal of current converter technology is to construct converters of small size and weight, which process substantial power at high efficiency

why

# Part I. Converters in equilibrium

Inductor waveforms



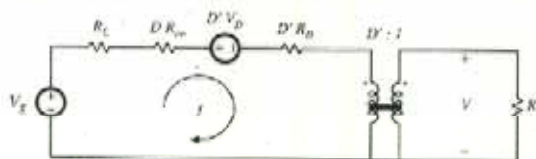
$\Delta i = \frac{1}{L}$  is Key

Discontinuous conduction mode

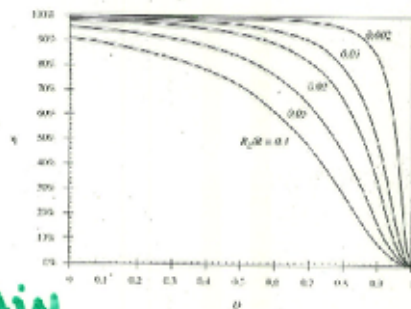
Transformer isolation

↑ explain

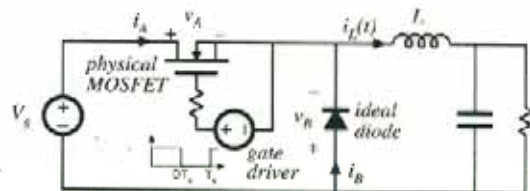
Averaged equivalent circuit



Predicted efficiency



### 4.3.1. Transistor switching with clamped inductive load



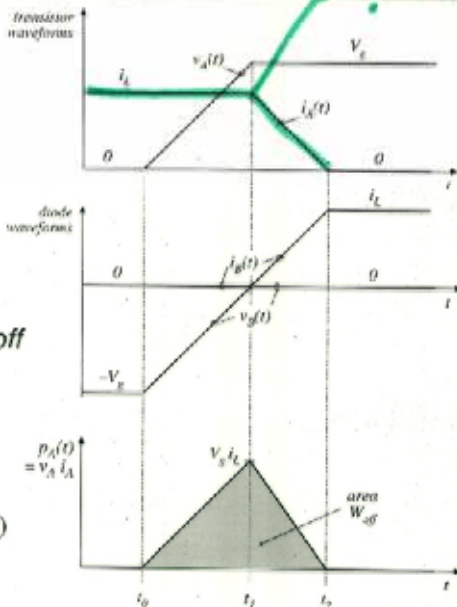
Buck converter example

$$v_B(t) = v_A(t) - V_g$$

$$i_A(t) + i_B(t) = i_L$$

transistor turn-off transition

$$W_{off} = \frac{1}{2} V_g i_L (t_2 - t_0)$$



## Switching loss induced by transistor turn-off transition

---

Energy lost during transistor turn-off transition:

$$W_{off} = \frac{1}{2} V_g i_L (t_2 - t_0)$$

Similar result during transistor turn-on transition.

Average power loss:

$$P_{sw} = \frac{1}{T_s} \int_{\text{switching transitions}} p_A(t) dt = \underbrace{(W_{on} + W_{off})}_{\text{Joules}} f_s$$



# Switch realization: semiconductor devices

Explain Shootthrough loss

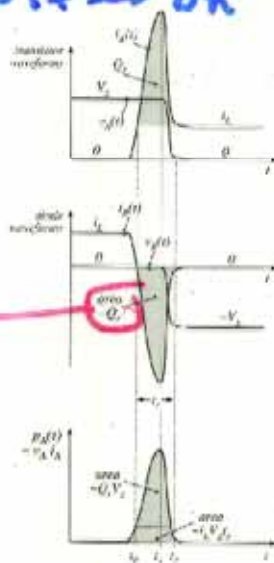
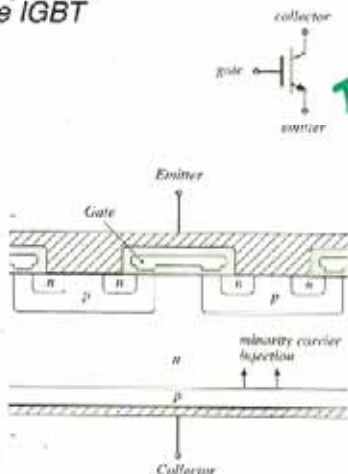
Q cycle is off  $\rightarrow$  on

Switching loss

D cycle is on  $\rightarrow$  off

Role of  $Q_{rr}$

The IGBT



Sw Losses limit  $f_{max}$

output switch  $\rightarrow$   $E_{sw} f_{sw} = P(\text{switch input})$

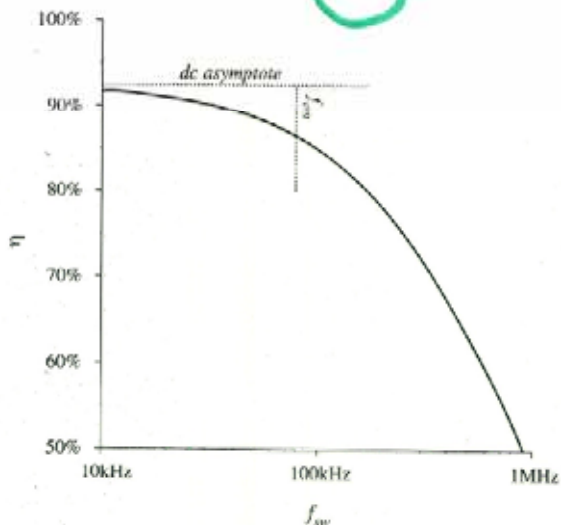
input switch  $\rightarrow$   $C_g V_g^2 f_{sw} = P(\text{input sw})$

Efficiency vs. switching frequency

be able to explain

$W_{tot}(\text{switch})$  R load  
L load

$$P_{loss} = P_{cond} + P_{fixed} + W_{tot} f_{sw}$$

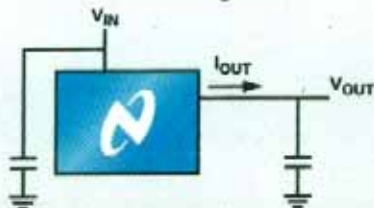


Switching losses are equal to the other converter losses at the critical frequency

$$f_{crit} = \frac{P_{cond} + P_{fixed}}{W_{tot}}$$

This can be taken as a rough upper limit on the switching frequency of a practical converter. For  $f_{sw} > f_{crit}$ , the efficiency decreases rapidly with frequency.

## Linear regulator



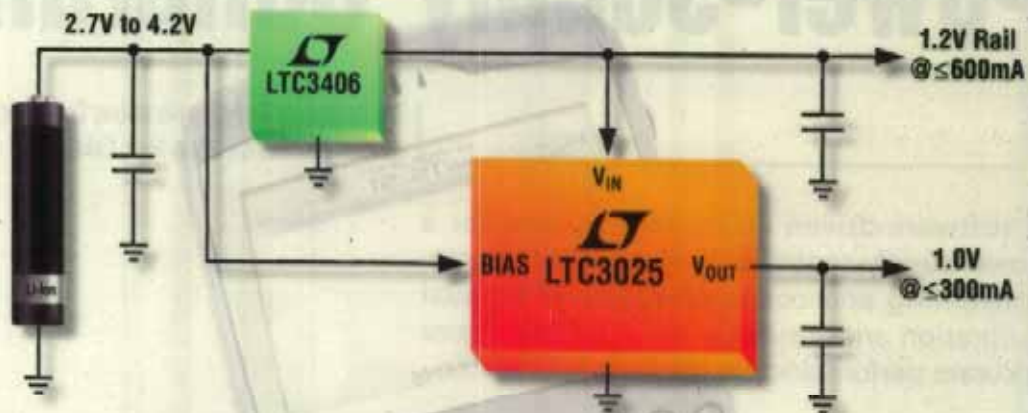
**Function:** Step-down ( $V_{OUT} < V_{IN}$ )

**When to use:** Typically when  $I_{OUT} < 1A$ , ultra low-dropout, and low-noise applications

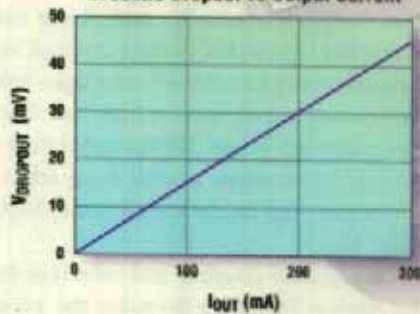
**Characteristics:** Excellent option where fixed output, low current, and low voltage drops are required. Easy to implement


**Devices to use:** Any low-dropout, linear regulator

**Comments:** Great for micropower applications



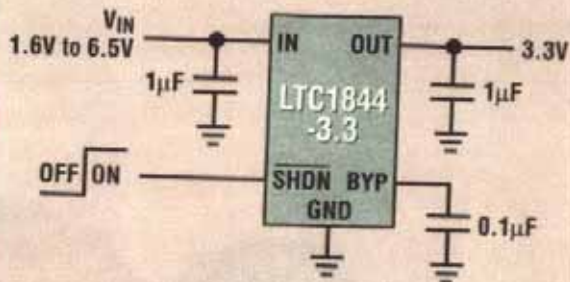
LTC3025 Dropout vs Output Current




  
 2mm x 2mm DFN  
 (Actual Size)

VLDO™ Regulators Down to  $V_{IN} = 0.9\text{V}$  and  $V_{OUT} = 0.2\text{V}$

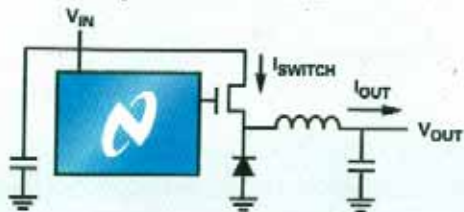
# Micropower, Low Noise, Ultra Low Dropout 150mA Linear Regulator



## Features:

- $V_{IN}$ : 1.6V to 6.5V
- Low 90mV Dropout at 150mA Output  
30mV Dropout at 50mA Output
- 40µA Supply Current
- Low Profile (<1mm) ThinSOT Package

## Non-synchronous buck

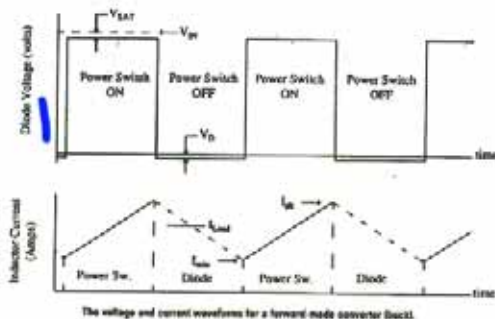


**Function:** Step-down ( $V_{OUT} < V_{IN}$ )

**When to use:** Typically when  $V_{IN}$  is 3x to 5x  $V_{OUT}$  and  $I_{OUT}$  is  $> 0.5A$  and  $< 5A$

**Characteristics:** Easy to design and good efficiency for the above-mentioned typical  $V_{IN}/V_{OUT}/I_{OUT}$  conditions

**Devices to use:** All buck integrated regulators and controllers

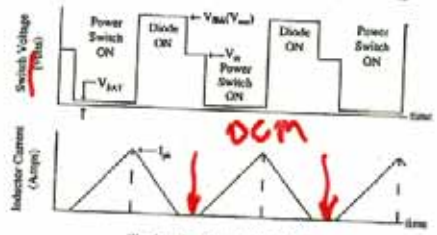
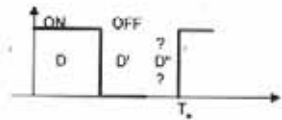


What if the inductor current wanted to go negative with a diode in the circuit?? What would occur??

## 2. DISCONTINUOUS CONDUCTION MODE

An extra period "d" occurs in the switching that is created by circuit topology conditions, not by the switch drive alone. For example, switches may close or open due to circuit conditions alone as when a diode will not conduct in the opposite direction even though the controlled switch asks it to do so. This change occurs during a portion of a switch time interval and is independent of gate signals. Another example would be a bipolar transistor which conducts in only one direction and not in the opposite as MOS transistors can do. The onset and duration of this "out of control" interval are not set by control switch signals but rather by the circuit conditions as we will see later.

Below we show this interval "d" and the associated switch voltage and inductor current waveforms.



labs  
 $R_L \rightarrow \infty$   
 always  
 DCM

Waveforms for a discontinuous-mode boost converter

NOTICE ABOVE THAT THE INDUCTOR CURRENT CANNOT GO NEGATIVE IN THIS TOPOLOGY AND CHOICE OF SWITCHES. Be careful, other circuit topologies and switch choices could allow a negative inductor current to occur.

FOR A SPECIFIC EXAMPLE, consider below the switch mode circuit where  $I$  (leakage) of a transformer causes a dead-time  $t_d$ , when  $I$  (leakage) discharges. This leakage inductor is not on your original circuit design. It is a parasitic element of the real transformer which modifies the time durations expected from switching via control signals alone.



## Features

- Complete, easy-to-use switcher solution has smallest footprint and highest power density in the industry
- State-of-the-art 13 ns minimum ON-time allows for high conversion ratios without the need to reduce switching frequency or increase solution size
- Choice of switching frequencies allows designers to trade off efficiency against solution size and EMI
- Current mode control improves phase margin, line regulation and rejection of transients
- PWM provides a predictable, easily filtered switching frequency for reduced output noise
- Internal softstart circuitry, cycle-by-cycle, thermal shutdown, and over-voltage protection
- Available in Thin SOT-23 packaging (1.0 mm height)

AVAILABLE  
LEAD-FREE

---

Ideal for systems that need to convert 3.3V, 5V, 12V, or 16V intermediate rails to 1.5V or less where solution size is critical

↑ buck

# Instant 10A Power Supply

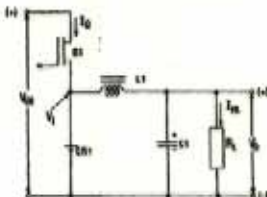


Complete, Quick & Ready.

## TYPE OF CONVERTER

Buck (Step Down)

## CIRCUIT CONFIGURATION



## IDEAL TRANSFER FUNCTION

$$\frac{V_O}{V_{IN}} = \frac{t_{on}}{T_S} = D$$

## PEAK DRAIN CURRENT

$$I_{D\text{MAX}} = I_{RL} + \frac{\Delta I_L}{2}$$

## PEAK DRAIN VOLTAGE

$$V_{D\text{S}} = V_{IN} + V_D$$

## AVERAGE DIODE CURRENTS

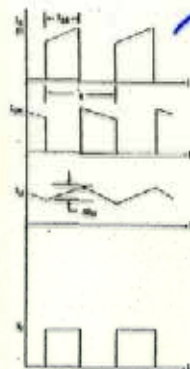
$$I_{CR1} = I_{RL} (1-D)$$

## DIODE VOLTAGES (VRM)

Exam

## VOLTAGE AND CURRENT WAVEFORMS

$$V_{IM} = V_{IN}$$



$I_{in}$   
EMC ?

$I_{out}$   
EMC

## ADVANTAGES

High efficiency, simple, no transformer, low switch stress. Small output filter, low ripple.

## DISADVANTAGES

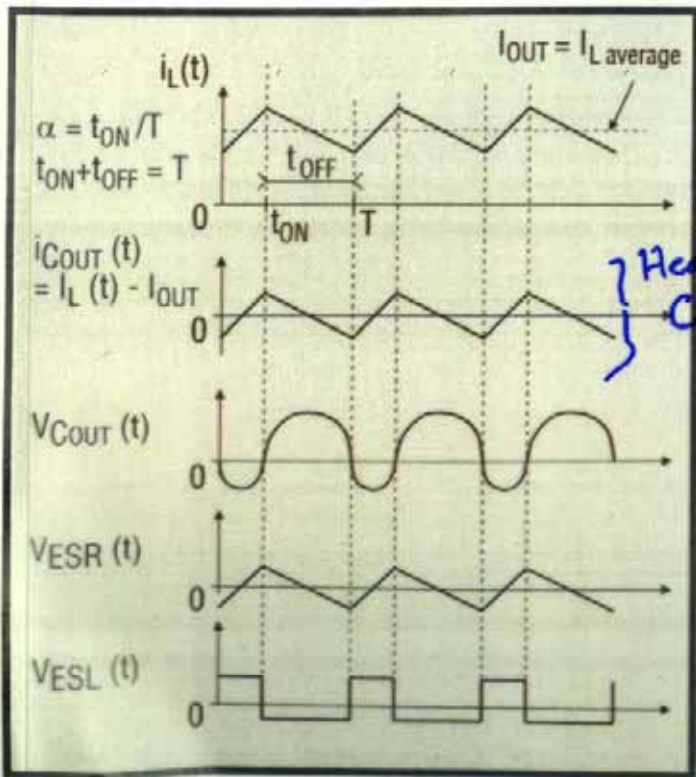
No isolation between input and output. Potential over-voltage if O1 shorts. Only one output possible. High-side switch drive required. High input ripple current.

## TYPICAL APPLICATIONS

Small size, embedded systems.

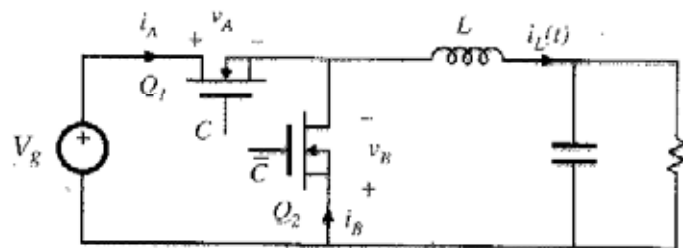
## APPLICABLE HARRIS PRODUCTS

HR10001, HR10002 For off line DC/DC.



**Fig. 3.** Currents in the inductor and filter capacitor ( $C_{OUT}$ ) of a buck converter.

## Buck converter with synchronous rectifier

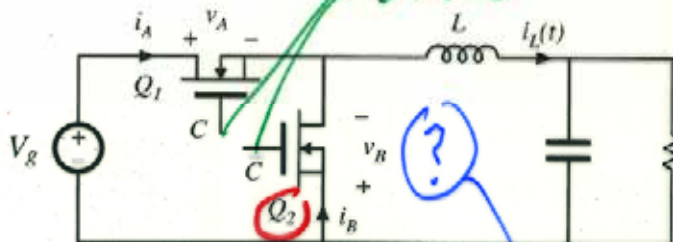


- MOSFET  $Q_2$  is controlled to turn on when diode would normally conduct
- Semiconductor conduction loss can be made arbitrarily small, by reduction of MOSFET on-resistances
- Useful in low-voltage high-current applications

# Buck converter with synchronous rectifier

known for boost too

Complementary Drive



MOSFET  $Q_2$  is controlled to turn on when diode would normally conduct

$i \uparrow \downarrow$

Semiconductor conduction loss can be made arbitrarily small, by reduction of MOSFET on-resistances

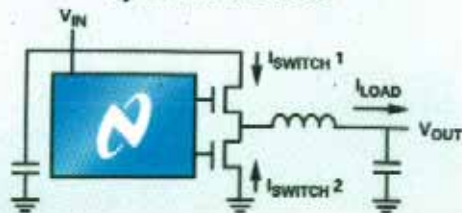
Useful in low-voltage high-current applications

$\mu P$  : Power Supply  
Chip : 1V, 100A

reality?

Inverter for solar cell at  $V_{out} = 1V$

## Synchronous buck



**Function:** Step-down ( $V_{OUT} < V_{IN}$ )

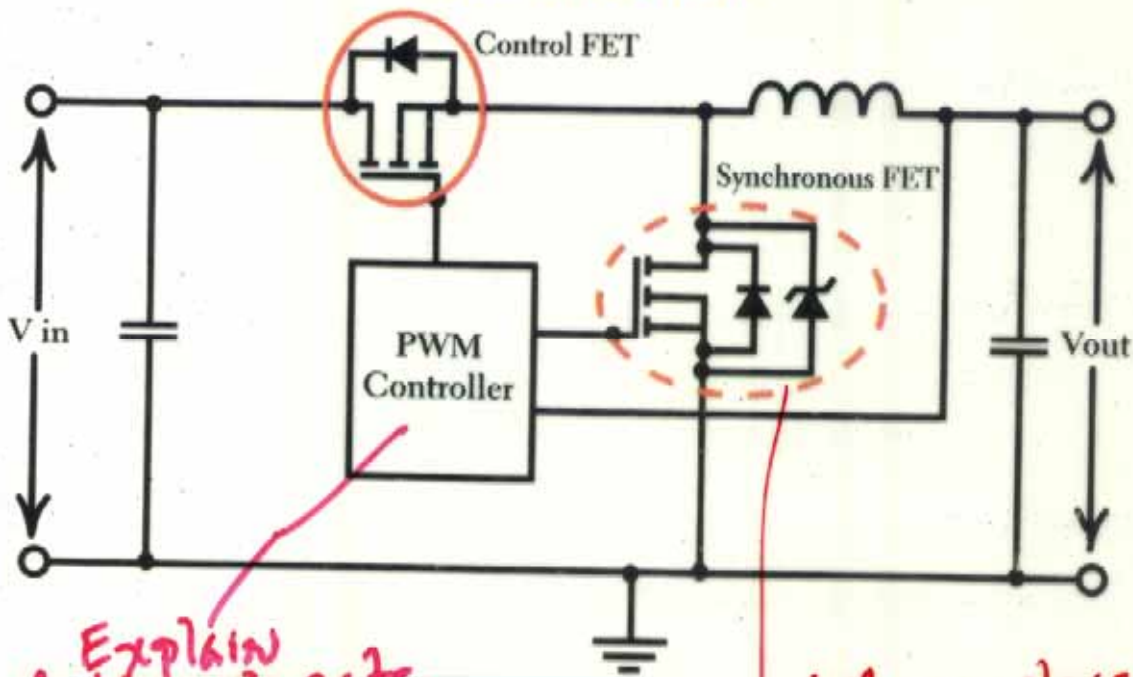
**When to use:** When high efficiency is required with high-output current ( $> 5A$ ) or low duty cycles ( $V_{IN} > 5 \times V_{OUT}$  and/or  $I_{OUT} < 0.5A$ )

**Characteristics:** A second switch replaces the diode in the basic buck topology, reducing losses in the conditions mentioned above

**Devices to use:** Any "synchronous rectification" buck integrated regulator or controller

**Figure 2: Step-down configurations**

# BUCK CONVERTER



Explain  
different gate  
drives

Why replace  
smart diode  
with FET



## 4.1.5. Synchronous rectifiers

Replacement of diode with a backwards-connected MOSFET, to obtain reduced conduction loss

$$V_{ON} = V_D + I R_D$$

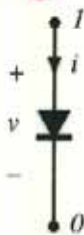
$$V_{ON} = I R_{(ON)} = 1V$$

*100k PS 10mA*

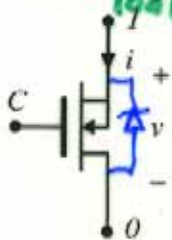
less  $V_{ON}$   
at High  
I  
e.g.  $\mu P$



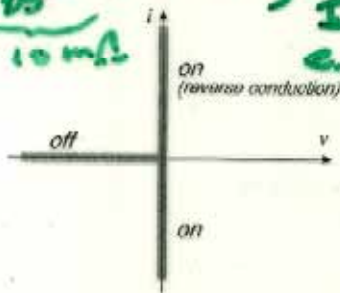
ideal switch



conventional diode rectifier



MOSFET as synchronous rectifier



instantaneous i-v characteristic

**Diode**

Fundamentals of Power Electronics

only has +v capability

**FET**

25

Chapter 4: Switch realization

has  $\pm i$  capability

## 4.1.5. Synchronous rectifiers

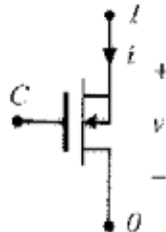
Replacement of diode with a backwards-connected MOSFET,  
to obtain reduced conduction loss



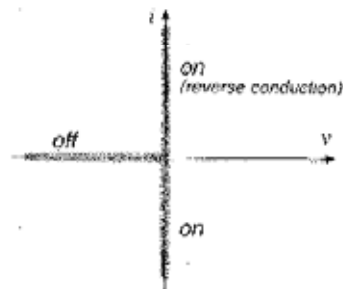
*ideal switch*



*conventional  
diode rectifier*



*MOSFET as  
synchronous  
rectifier*



*instantaneous i-v  
characteristic*

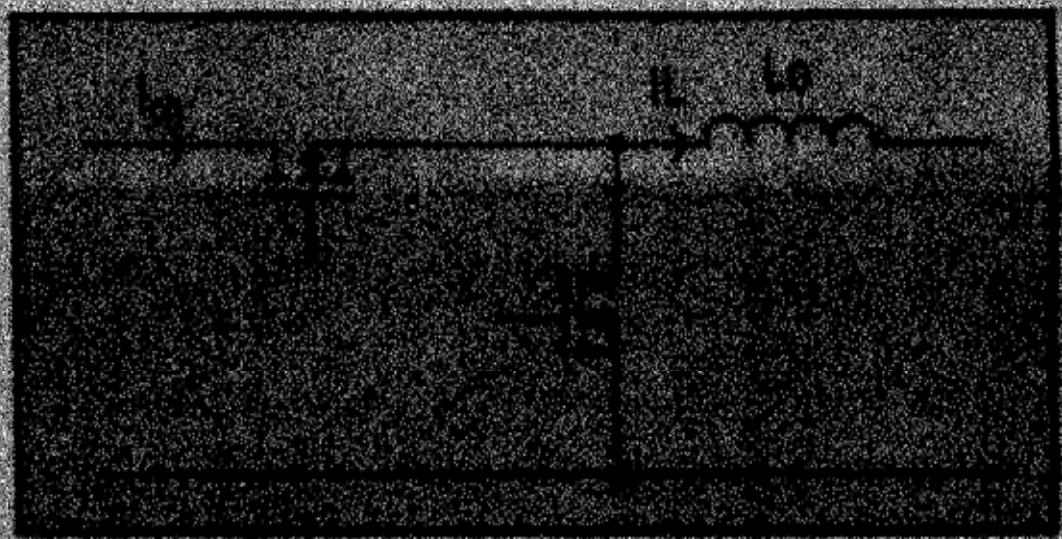
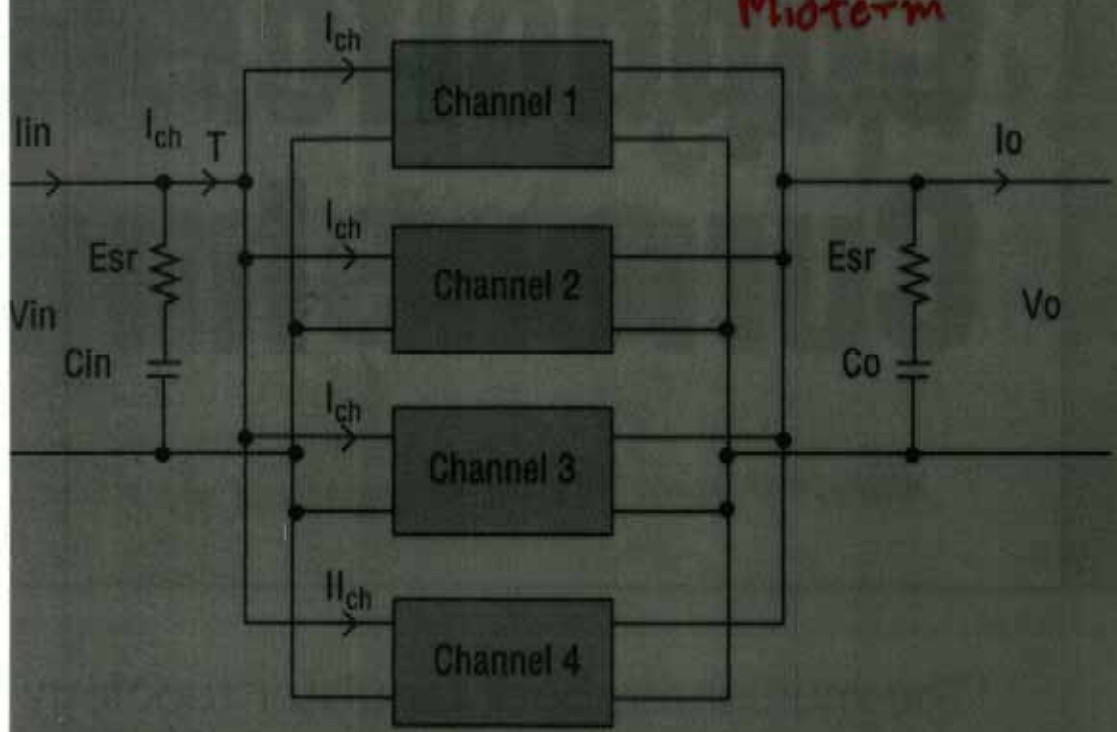


Fig. 4. Basic channel circuit.

Midterm



5. Four-channel multiphase buck regulator.

மிட்டர்

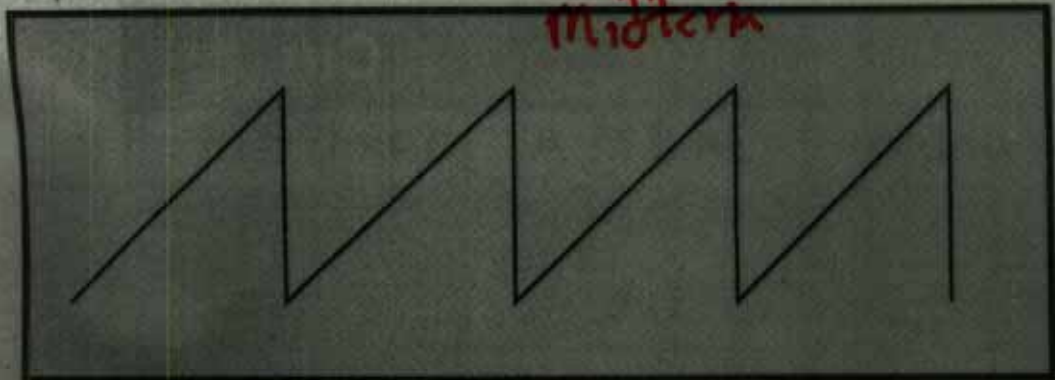


Fig. 6. Input current for  $N \cdot D = \text{integer}$ .

a  
ez  
qu  
tin  
fre  
ot  
th  
gr

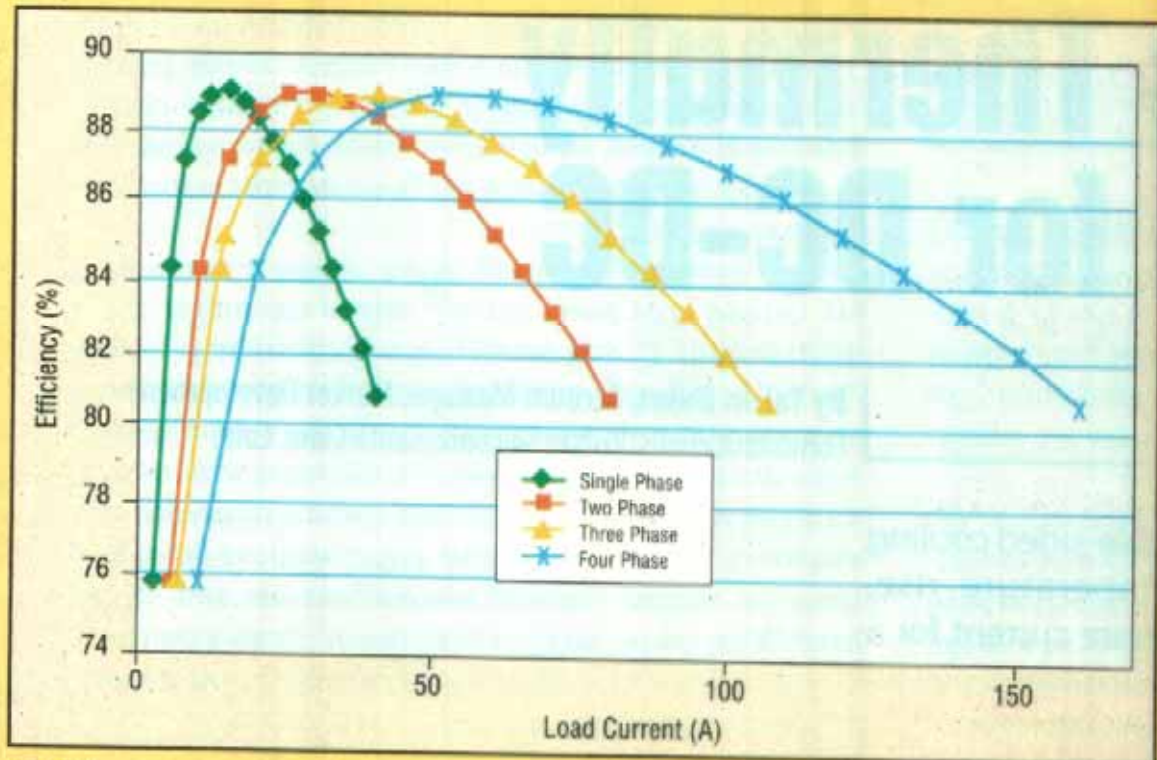
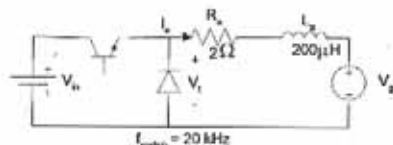


Fig. 7. Power-conversion efficiency for different implementations of the VRM design.

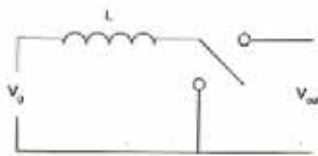


One can show for  $R_s$  small,  $V_o$  should be the average value of  $V_1 = D_1 V_{in}$ , hence we can control motor speed by varying either  $V_{in}$  or  $D$ :  $w(\text{motor}) = DV_{in}/k$

c. Boost Circuit Topology

*radians/sec*

The left side of  $L$  is fixed at  $V_o$  (raw dc) and the right side of  $L$  is switched from  $V_{in}$  to ground. Again  $L$  keeps KVL violations from occurring during switching.



For the switch to ground:

$$s_u = \frac{V_g}{L} \text{ (A/s)}$$

For the switch to  $V_o$ :

$$s_d = \frac{-(V_g - V_o)}{L} \text{ (A/s)}$$

Here,  $V_o/V_{in} = 1/(1-D) = 1/D'$ . This gives output greater than input.

**Boost Example:**

$$V_o = 20, V_{in} = 50 \\ \Rightarrow V_o/V_{in} = 1/D' = 1/0.4, D = 0.6$$

Unique  $f(D)$  for Boost topology

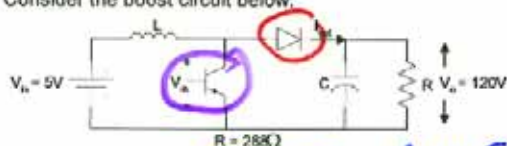
$$\text{Verify } s_u/s_d = D/D' = 0.6/0.4 = 1.5 = 30/20$$

We can consider the I as transforming  $V_g$  into a current source input to the switch to achieve:

$$V_{out} = V_{in}/(1-D) \text{ and } I_{in} = I_{out}/(1-D).$$

Note  $P_{in} = P_{out}$  both on average and instantaneously, as we assumed zero losses in the converter switches as well as L-C components.

Consider the boost circuit below:



Goal:

$$V_{in} = 5V \text{ but } I_{in} \text{ fixed } \pm 0.1\%$$

$$V_o = 120V \pm 0.1\%$$

$$P_{in} = P_o = 50W$$

Hence for zero loss  $I_{in} = 10A$  and  $I_o = 0.42$  ideal

$f_{sw}$  is fixed at 20 kHz

or  $T_{sw} = 50 \mu s$

Solution  $\Rightarrow$  The off time of the switch transistor is:

$$D' = 5/120 = 0.042 \quad \text{Recall } D + D' = 1$$

So the diode is on for  $(0.042/50) = 2.1 \mu s$  out of  $50 \mu s$  and the transistor is on for  $47.9 \mu s$ . This makes sense as we need more time to build from 5V to 120V than to discharge the 120V.

For the lossless operation  $I_{in} = 50/5 = 10A$  and we specify  $\Delta I_{in} = \pm 0.1\% = \pm .1A$ . This  $\Delta I$  specification sets the L choice

$$e = L di/dt (\text{on time of transistor})$$

$$5 = L (0.2 A/47.9 \mu s)$$

$L > 1.2 \text{ mH}$  to insure  $\Delta I_{in} < 0.01$ . For lossless operation

$$I_{out} = 50/120 = 0.42 \text{ A. Our } \Delta V_{out} = 120 \pm 0.01 = \pm 1.2V$$

this  $\Delta V_c$  sets the C choice

*low EMC on input i*



# 5A, 40V Boost Regulators

$f_{sw} \sim 3.5 \text{ MHz}$



High Frequency, Monolithic, Efficient & Tiny

## Featured Boost Regulators

Part No.	V <sub>IN</sub> Range	V <sub>OUT</sub> Max.	I <sub>(sw)</sub>	Switching Frequency	Package
<b>T<sup>®</sup>1935</b>	2.3V to 16V	38.0V	2A	1.2MHz	ThinSOT™
<b>T3489</b>	2.4V to 16V	38.0V	2.5A	2.2MHz	MS8E
<b>T3477</b>	2.5V to 25V	40.0V	3A	3.5MHz	4mm x 4mm QFN-20, TSSOP-20E
<b>T3479</b>	2.5V to 24V	40.0V	3A	3.5MHz	4mm x 3mm DFN-14 TSSOP-16E
<b>T1370HV</b>	2.7V to 30V	40.0V	6A	500kHz	TO-220, TO-263

$$i = C \, dv/dt(\text{off time of the diode}) \quad \Delta V \text{ spec}$$

$$0.42 = C \cdot 2.4/2.1 \, \mu\text{s}$$

$$C > 83.3 \, \mu\text{F for } \Delta V_o < 0.01$$

Finally prove to your self that a +50 ns time jitter on the transistor switch time causes  $V_{out}$  to vary from 117 to 123 V or  $\pm 2.5\%$ .

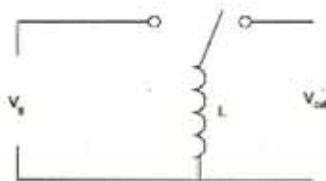
on/off timing

#### d. Buck-Boost Circuit Topology

Bottom of L is fixed at ground while the top side switches from  $V_g$  to  $V_{out}$ . For the case of feedback in the circuit,  $V_g$  could be crude rectified DC and  $V_o$  regulated DC.

Here  $V_o/V_{in} = D/D'$  and the output is opposite polarity to the input moreover we overcome the  $V_{out} < V_{in}$  limitation of the buck and the  $V_{out} > V_{in}$  limitation of the boost. No KVL violations occur as each voltage supply only sees L which appears as a current source.

For analysis below we assume both do not vary over  $T_s$ .



Switch at  $V_g$ :

$$s_{ij} = \frac{V_g}{L} \text{ (A / s)}$$

Switch at  $V_{out}$ :

$$s_{id} = \frac{V_{out}}{L} \text{ (A / s)}$$

$s_i$  very fixed for feedback case

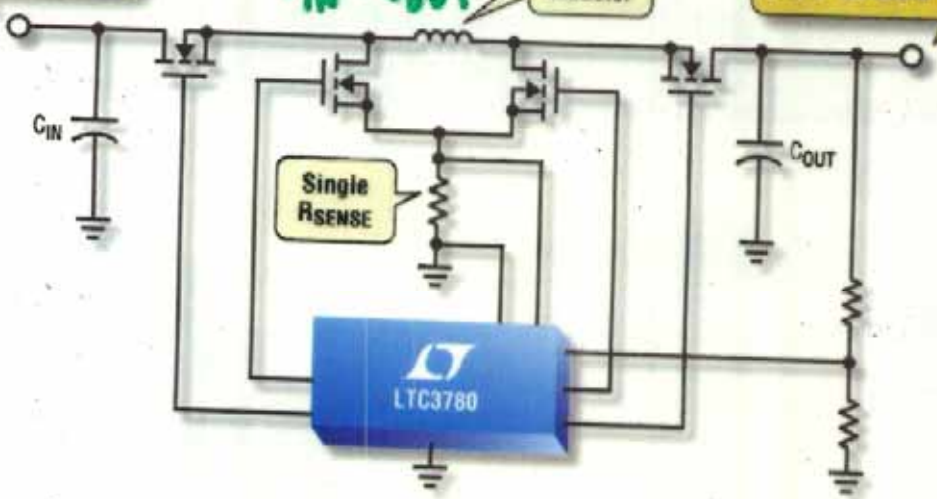
$$\Rightarrow \frac{V_{out}}{V_g} = \frac{-D}{D'}$$

Variable Input  
4V to 36V

w.r.t. ground  
Switch  $V_L$   
 $V_{IN} \rightarrow V_{OUT}$

Single Inductor

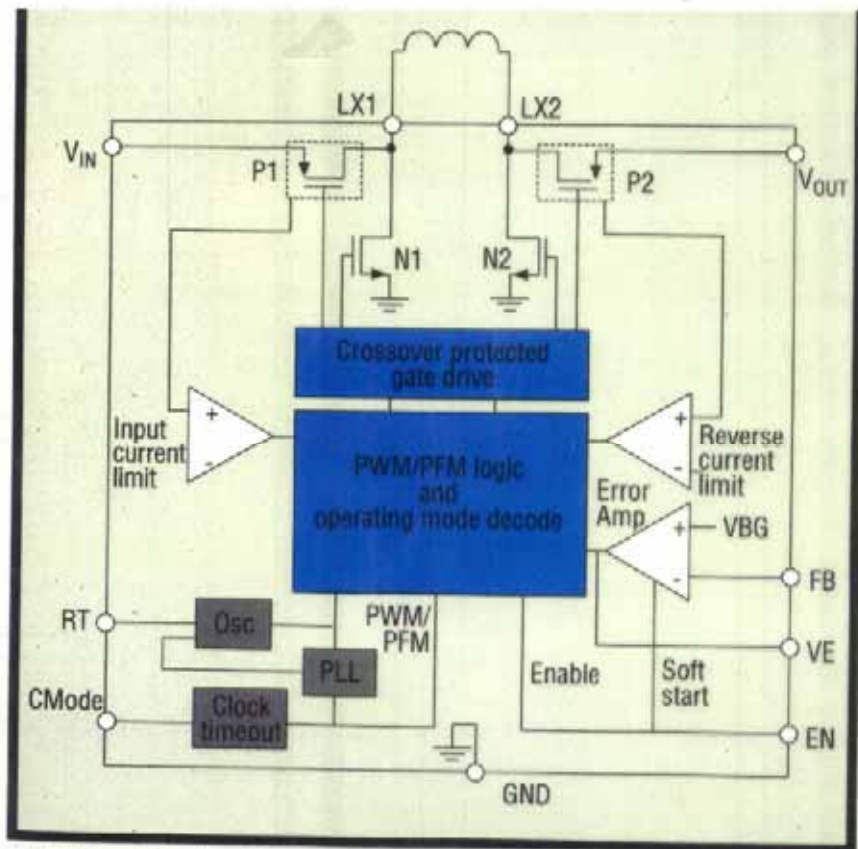
Fixed Output  
0.8V to 30V @  $\leq 10A$



Single RSENSE



# 97% Efficient Single Inductor Buck-Boost Controller

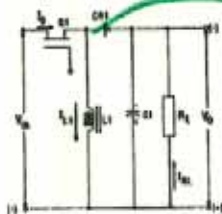


*A noninverting buck-boost converter incorporates n- and p-channel power MOSFETs.*

## TYPE OF CONVERTER

## CIRCUIT CONFIGURATION

Buck - Boost (Step Down/Up)



$$\frac{V_O}{V_{IN}} = - \left( \frac{t_{on}}{T_s - t_{on}} \right) = - \left( \frac{D}{1-D} \right)$$

$$I_{DMAX} = I_{RL} \left( \frac{1}{1-D} \right) + \frac{\Delta I_L}{2}$$

$$V_{DS} = V_{IN} + V_O + V_D$$

$$I_{CR1} = I_{RL}$$

## IDEAL TRANSFER FUNCTION

## PEAK DRAIN CURRENT

## PEAK DRAIN VOLTAGE

## AVERAGE DIODE CURRENTS

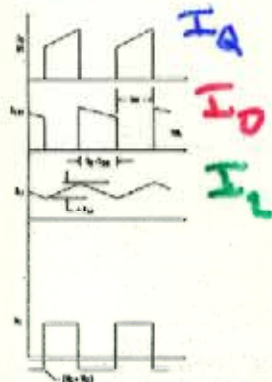
## SWITCH VOLTAGES (VRM)

Sw.  $V_{cs}$ :  $V_{IN}$   
 $V_{OUT}$

$$V_{RM} = V_O + V_{IN}$$

w.r.t. ground

## VOLTAGE AND CURRENT WAVEFORMS



## ADVANTAGES

## DISADVANTAGES

## TYPICAL APPLICATIONS

## APPLICABLE HARRIS PRODUCTS

Voltage inversion without using a transformer, simple, high frequency operation.

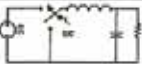
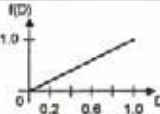

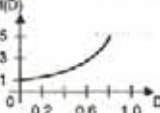
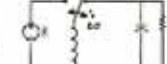
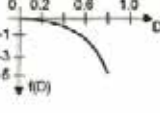
No isolation between input and output. Only one output is possible. Regulator loop hard to stabilize. High-side switch drive required. High output ripple. High input ripple current.

Inverse output voltages.

# EMC for

L8

## SIMPLE SWITCH MODE CONVERTERS

 <p><b>BUCK</b> The buck is limited in that <math>V_{out} &lt; V_{in}</math> only</p>		<p><math>V_L = V_o</math> or <math>V_{DC} - V_o</math>  <math>f(D) = D</math>                      • Never get negative output  <math>V_o(\min) = 0</math></p>
 <p><b>BOOST</b> The boost is limited in that <math>V_{out} &gt; V_{in}</math> only</p>		<p><math>f(D) = 1/(1-D)</math>                      • Never get zero output:  <math>V_o(\min) \neq 0</math>  <math>V_L = V_{DC} - V_o</math> or <math>V_o</math></p>
 <p><b>BUCK-BOOST</b> The buck-boost is limited to negative voltages</p>		<p><math>f(D) = -D/(1-D)</math>                      • Inverting output w.r.t. <math>V_g</math>  <math>V_L = V_o = -V_{DC}</math>                      for <math>D = 1/2</math></p>

### 4. UNSYMMETRIC $i_L$ AND $v_C$ WAVEFORMS OF EQUAL INTEGRATED AREA IN THE ABOVE THREE CONVERTERS

EMC high!  
 Why?  
 How to reduce

### Common-Mode Noise Vicor 48:12 Bus Converter Module Compared to PWM Supply



5(a) 100 kHz PWM Forward



5(b) 3.5 MHz Vicor BCM

supplies. It will then be possible to do three-stage power conversion (PFC, Bus Conversion, Point-of-Load Conversion) that will exceed the performance of conventional power supply designs.



# Review 2L/2C Converters } SAME for lower EMC/EMI } DC

transfer function follows the same principles. All provide  $V_{out}$  from 0 to  $nV_g$ .

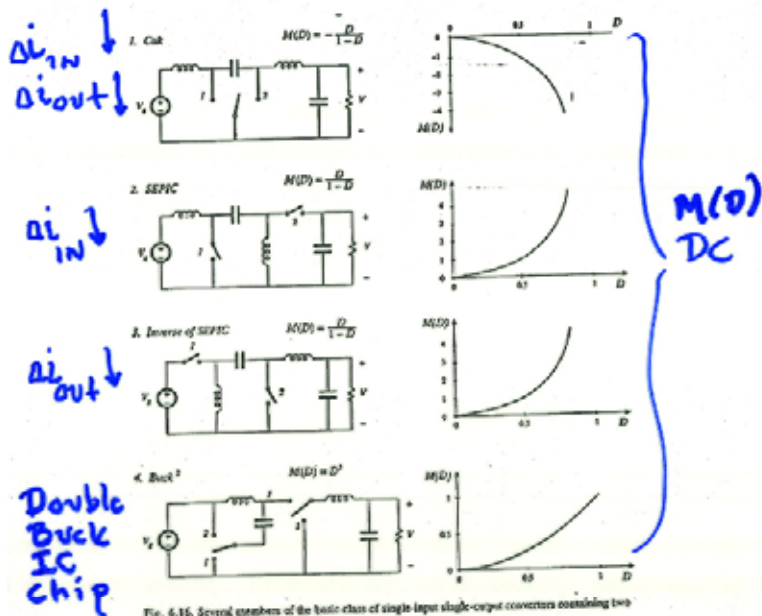
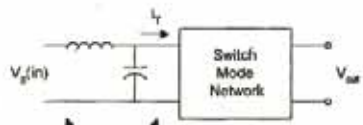


Fig. 4.16. Several members of the basic class of single input single output converters consisting two inductors.

Note: Of the above, only Cuk has negative  $V_{out}$  w.r.t.  $V_g$  and  $V_{out}$  varies from 0 to  $M(D)V_g$  for all of the double L/C converters.

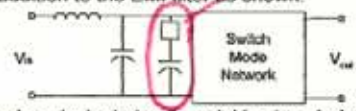
## B. Lossless Cuk converter $M(D)$ analysis



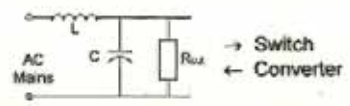
Input filter to keep switching harmonics from entering the mains: EMI noise issue is now a legal one.

*Ch 10  
Why needed?  
R (switch) is in mode?  
?*

Incidentally, some switching signals have very sharp transients that can be better reduced by an RC snubber circuit in addition to the EMI filter as shown:



A simplified equivalent circuit model is given below:



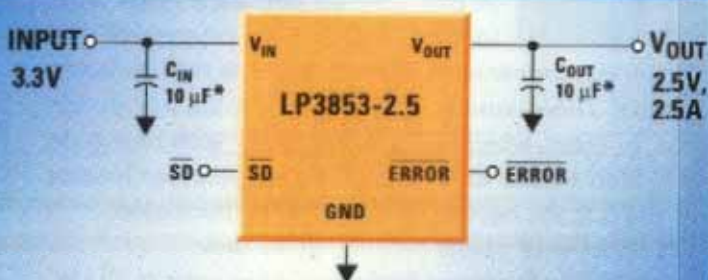
Previously, we saw that for an equal duration square wave ( $d = d'$ ) the fundamental ac component of the signal has an amplitude  $2V_{od}/\pi$ . We will consider the attenuation of the filter in two ways on the next page:

- A rough transfer function approach
- An intuitive ripple estimate approach

## LP385x family of CMOS low dropout regulators

Part number	Output current (A)	Features
LP3852	1.5	On/off, error flag
LP3855	1.5	On/off, separate sense pin, ADJ
LP3853	3.0	On/off, error flag
LP3856	3.0	On/off, separate sense pin, ADJ

### LP3853 typical application diagram



\*Ceramic, tantalum, or aluminum electrolytics

# LP3853 dropout voltage vs. output load current

$$V_{OUT} = 2.5V$$

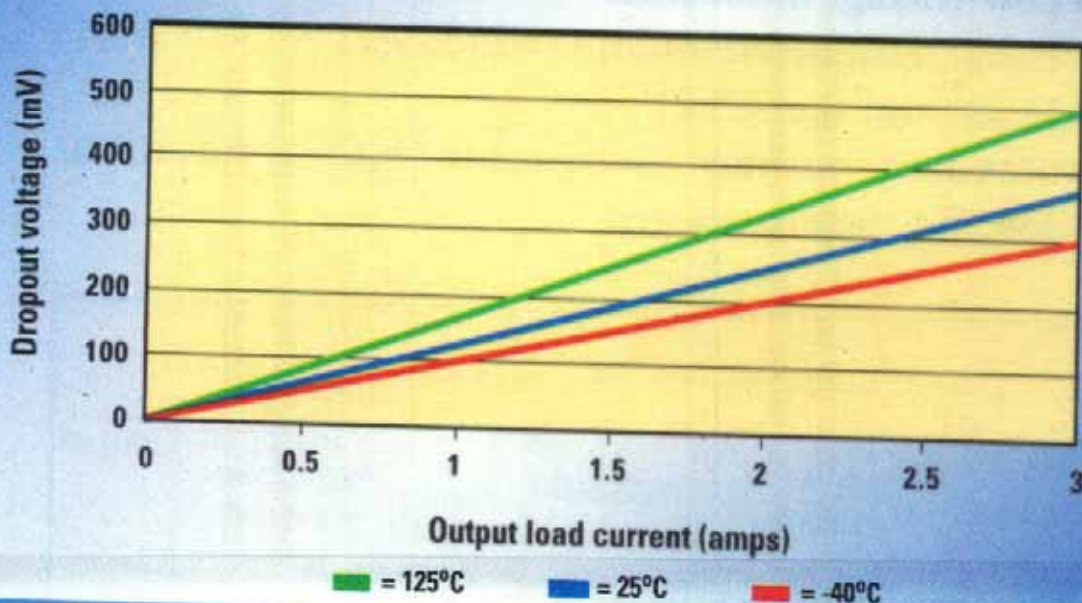




Figure 3-10 The SOPS overall site a best test

1. Output power

$$P_{out} = \sum_{n=1}^n (V_{out(n)} I_{out(n)})$$

2. Input power

$$P_{in} = \frac{P_{out}}{\text{out. efficiency}}$$

3. Average input current

$$I_{in(average)} = \frac{P_{in}}{V_{in(average)}}$$

4. The input peak current

This is completely determined by the topology

$$I_{in} = \frac{k P_{out}}{V_{in(peak)}}$$

where  $k = 1.4$  for the buck, and full-bridge  
 $= 2.8$  for the half-bridge, and half-bridge  
 $= 5.5$  for the boost, buck-boost, and flyback

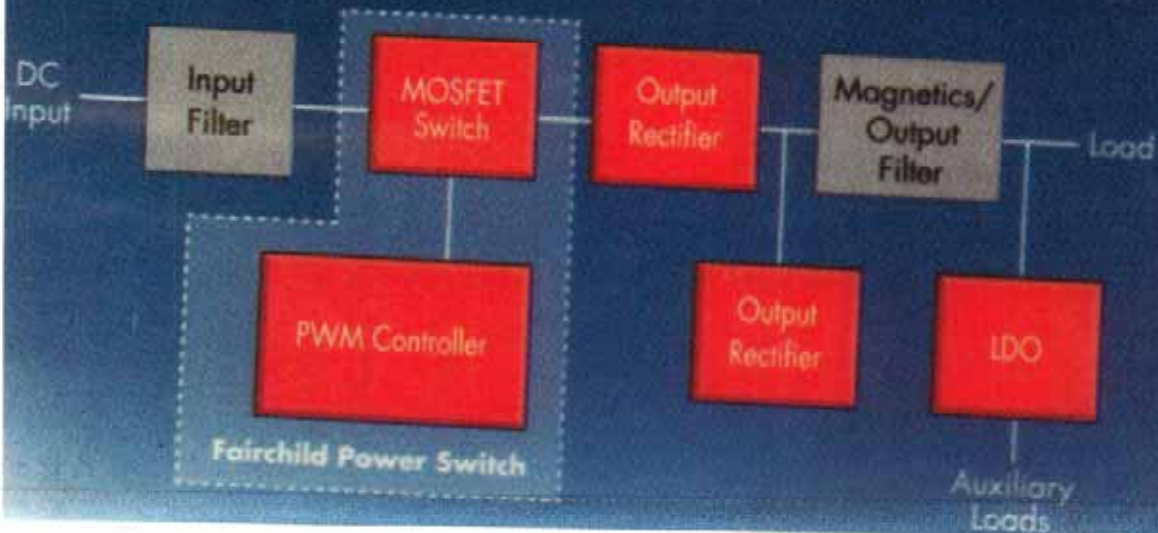
The value of the peak current is useful in the design of the diode-code inductors and transformers. For the forward-mode supplies,  $k$  is just a constant or this etc.

Hopefully this tilted overview will be kept in mind as we proceed in the course. Each subsection takes so much effort that we easily lose the overall goal.

In section B we start the introduction to the waveforms found in the PWM converters. Their shape and their mathematical representation. Again this material is meant

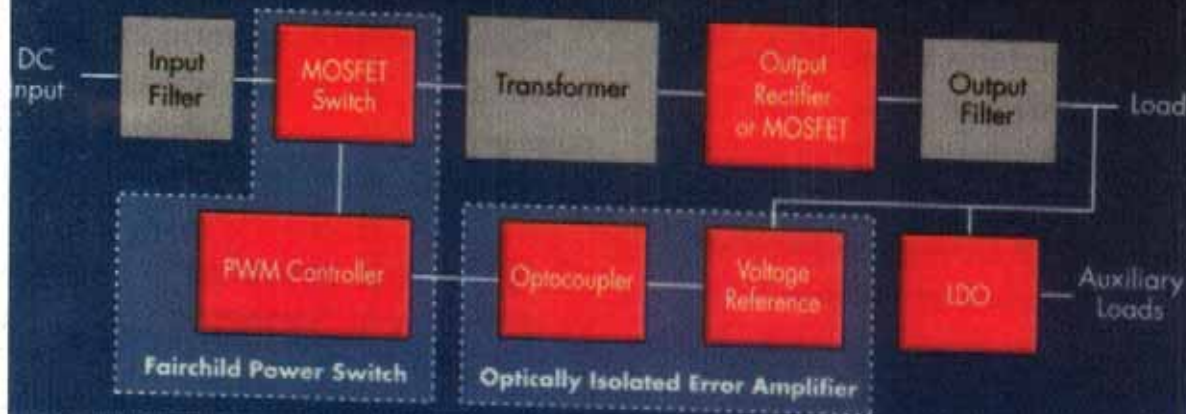
Ch 1-3

## Non-Isolated DC/DC Power Supply



Ch 6

## Isolated DC/DC Power Supply



## Primary and Secondary Side DC/DC Controllers

Function	Part Number	Description	Package
Flyback Controllers	LTC®3803	Constant Frequency 200kHz with adjustable slope-compensation	ThinSOT™
	LT®1725	No optoisolator required; Senses $V_{OUT}$ from primary side winding	SO-16, SSOP-16
	LT1737	No optoisolator required; Low 4.5V <sub>MIN</sub> supply voltage	SO-16, SSOP-16
Single Switch Forward Controllers	LT1952	Synchronous; Programmable volt-second clamp	SSOP-16
	LT1950	3V to 25V input voltage range; Onboard auxiliary boost converter	SSOP-16
	LTC3900	Secondary side synchronous rectifier driver for forward controllers	SO-8
2-Switch Forward Controllers	LTC3705	PolyPhase™, No need for separate bias regulator	SSOP-16
	LT3781	72V operation; Synchronizable for multiple controller systems	SSOP-20
Push-Pull Half- & Full-Bridge PWM Controllers	LTC3723	Synchronous; Adjustable dead-time and synchronous timing	SSOP-16
	LTC3721-1	Adjustable dead-time; 4mm x 4mm QFN package	SSOP-16, QFN
	LTC3901	Secondary side synchronous driver for push-pull and full-bridge	SSOP-16
Full-Bridge ZVS Controller	LTC3722	Current and voltage mode with adaptive or manual delay control for zero voltage switching	SSOP-24
Secondary Side 2-Switch Forward Controllers	LTC3706	Fast, PolyPhase current mode	SSOP-24
	LTC1698	Secondary side synchronous rectifier controller	SO-16
Secondary Side Post Controllers	LT3710	Regulated auxiliary output in isolated DC/DC converters; Synchronous drivers; Programmable current limit	TSSOP-16
	LT3804	Regulates two secondary outputs; Integrated optocoupler driver	TSSOP-28
MOSFET Drivers	LTC4440	80V operation; 100V transient tolerant; Fast gate drive	ThinSOT, MSOP-8
	LTC4441	6A peak output current; 5V to 8V adjustable gate drive	MSOP-10; SO-8
	LTC1693	Single & dual N-, P-channel MOSFET drivers	SO-8, MSOP-8
Optocoupler Driver	LT4430	600mV, 1% accurate reference; prevents overshoot	ThinSOT
Overvoltage Protection Controller	LTC1696	±2% overvoltage threshold accuracy; Gate drive for SCR crowbar or N-channel disconnect MOSFET; Monitors two output voltages	ThinSOT



## TYPE OF CONVERTER

SEPIC (Step Down/Up)

## CIRCUIT CONFIGURATION



## IDEAL TRANSFER FUNCTION

$$\frac{V_O}{V_I} = \frac{D}{1-D}$$

non-inverting

## PEAK DRAIN CURRENT

$$I_{D\text{MAX}} = I_1 + I_{RL} + \frac{\Delta I_{L1}}{2} + \frac{\Delta I_{L2}}{2}$$

## PEAK DRAIN VOLTAGE

$$V_{DS} = V_O + V_{IN} + V_D$$

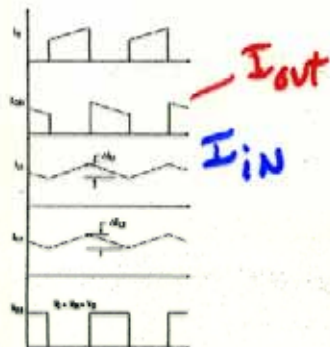
## AVERAGE DIODE CURRENTS

$$I_{CR1} = I_{RL}$$

## DIODE VOLTAGES (VRM)

$$V_{RM} = V_O + V_{IN}$$

## VOLTAGE AND CURRENT WAVEFORMS



## ADVANTAGES

Low ripple input current, step-up or step-down with no inversion, no transformer. Capacitive isolation protects against switch failure (unlike Buck).

## DISADVANTAGES

No isolation between input and output. Switch has high peak and rms currents which limit output power, C1 and C2 have high ripple current requirements (low ESR), continuous current mode makes loop stabilization difficult, potential instabilities with current-mode control. High output ripple.

## TYPICAL APPLICATIONS

Power-factor correction. High reliability. Wide input voltage range.

## APPLICABLE HARRIS PRODUCTS

1HP5060, 1HP5061, 1HP5062, 1HP5063

# P2m Ch4 #5 Next time

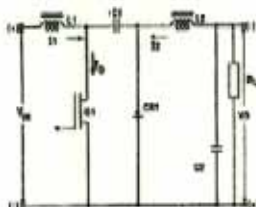
**TYPE OF CONVERTER**

CUK (Step Up/Down)

**VOLTAGES (VRM)**

$$V_{RM} = V_O + V_{IN}$$

**CIRCUIT CONFIGURATION**



**IDEAL TRANSFER FUNCTION**

$$\frac{V_O}{V_{IN}} = -\left(\frac{t_{on}}{T_S - t_{on}}\right) = -\left(\frac{D}{1-D}\right)$$

**PEAK DRAIN CURRENT**

$$I_{D_{MAX}} = I_1 + I_2 = I_1 \left(\frac{1}{D}\right)$$

**PEAK DRAIN VOLTAGE**

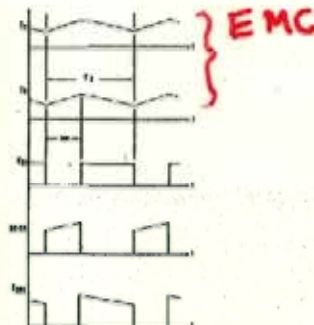
$$V_{DS} = 2 V_{IN}$$

**AVERAGE DIODE CURRENTS**

$$I_{CR1} = I_1 + I_2$$

$$I_1 + I_2 = I_1 \left(\frac{1}{D}\right)$$

**VOLTAGE AND CURRENT WAVEFORMS**



**ADVANTAGES**

Simple, low ripple input and output current, capacitive isolation protects against switch failure.

**DISADVANTAGES**

High drain current, C1 has high ripple current requirement (low ESR), high voltage required for D1, Voltage inversion.

**TYPICAL APPLICATIONS**

Low noise, inverter output voltages.

**APPLICABLE HARRIS PRODUCTS**

HP5080, HP5081, HP5082, HP5083