

L1D PbM4.4 LDC Choice

Vout	Duty Cycle	Vg	Road High 5000			Road Med 2500			Road Low 500		
			Vc1	Vc2	I1	Vc1	Vc2	I1	Vc1	Vc2	I1
5000	0	5000.00000	5000.000	5000.000	1.0000	5000.000	5000.000	2.0000	5000.000	5000.000	10.0000
5000	0.1	5625.00000	6250.000	5000.000	0.8889	6250.000	5000.000	1.7778	6250.000	5000.000	10.0000
5000	0.2	6666.66667	8333.333	5000.000	0.7500	8333.333	5000.000	1.5000	8333.333	5000.000	10.0000
5000	0.3	8750.00000	12500.000	5000.000	0.5714	12500.000	5000.000	1.1429	12500.000	5000.000	10.0000
5000	0.4	15000.00000	25000.000	5000.000	0.3333	25000.000	5000.000	0.6667	25000.000	5000.000	10.0000
5000	0.5	#DIV/0!	#DIV/0!	5000.000	0.0000	#DIV/0!	5000.000	0.0000	#DIV/0!	5000.000	10.0000
5000	0.6	-10000.00000	-25000.000	5000.000	-0.5000	-25000.000	5000.000	-1.0000	-25000.000	5000.000	10.0000
5000	0.7	-9750.00000	-12500.000	5000.000	-1.3333	-12500.000	5000.000	-2.6667	-12500.000	5000.000	10.0000
5000	0.8	-1666.66667	-8333.333	5000.000	-3.0000	-8333.333	5000.000	-6.0000	-8333.333	5000.000	10.0000
5000	0.9	-625.00000	-5000.000	5000.000	-8.0000	-6250.000	5000.000	-16.0000	-6250.000	5000.000	10.0000
5000	1	0.00000	#DIV/0!	5000.000	1.0000	#DIV/0!	5000.000	2.0000	#DIV/0!	5000.000	10.0000
5000	0	5000.00000	0.000	5000.000	0.0000	5000.000	5000.000	0.0000	5000.000	5000.000	10.0000
5000	0.1	5625.00000	-0.111	6250.000	-0.222	6250.000	5000.000	-1.111	6250.000	5000.000	10.0000
5000	0.2	6666.66667	-0.250	8333.333	-0.500	8333.333	5000.000	-2.500	8333.333	5000.000	10.0000
5000	0.3	8750.00000	-0.429	12500.000	-0.857	12500.000	5000.000	-4.286	12500.000	5000.000	10.0000
5000	0.4	15000.00000	-0.667	25000.000	-1.333	25000.000	5000.000	-6.667	25000.000	5000.000	10.0000
5000	0.5	#DIV/0!	#DIV/0!	25000.000	#DIV/0!	#DIV/0!	25000.000	#DIV/0!	#DIV/0!	25000.000	10.0000
5000	0.6	-10000.00000	-1.500	-25000.000	-3.000	-25000.000	5000.000	-15.000	-25000.000	5000.000	10.0000
5000	0.7	-9750.00000	-2.333	-12500.000	-4.667	-12500.000	5000.000	-23.333	-12500.000	5000.000	10.0000
5000	0.8	-1666.66667	-4.000	-8333.333	-8.000	-8333.333	5000.000	-40.000	-8333.333	5000.000	10.0000
5000	0.9	-625.00000	-9.000	-6250.000	-18.000	-6250.000	5000.000	-90.000	-6250.000	5000.000	10.0000
5000	1	0.00000	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!	10.0000

The voltage standoff required for the switches and the caps is independent of the load resistance.

To cover the 25 kV present on C1 if we use a 0.4 or 0.6 duty cycle we can stack 3 10kV Nova caps. See attached data sheet.

For the switches we can either use a couple of 12kV Thyristors or 6 HVIGBTs/AE stacked. This will depend on switch speed requirements

Vect	Duty Cycle	Vg	Road High 5000				Road Med 2500				Road Low 500			
			Vc1	Vc2	I1	I2	Vc1	Vc2	I1	I2	Vc1	Vc2	I1	I2
500	0	500.00000	500.000	500.000	0.1000	0.1000	500.000	500.000	0.2000	0.2000	500.000	500.000	1.0000	1.0000
500	0.1	562.50000	625.000	500.000	0.0889	0.1000	625.000	500.000	0.1778	0.2000	625.000	500.000	0.8889	1.0000
500	0.2	666.66667	833.333	500.000	0.0750	0.1000	833.333	500.000	0.1500	0.2000	833.333	500.000	0.7500	1.0000
500	0.3	875.00000	1250.000	500.000	0.0571	0.1000	1250.000	500.000	0.1143	0.2000	1250.000	500.000	0.5714	1.0000
500	0.4	1500.00000	2500.000	500.000	0.0333	0.1000	2500.000	500.000	0.0667	0.2000	2500.000	500.000	0.3333	1.0000
500	0.5	#DIV/0!	#DIV/0!	500.000	0.0000	0.1000	#DIV/0!	500.000	0.0000	0.2000	#DIV/0!	500.000	0.0000	1.0000
500	0.6	-1000.00000	-2500.000	500.000	-0.0500	0.1000	-2500.000	500.000	-0.1000	0.2000	-2500.000	500.000	-0.5000	1.0000
500	0.7	-375.00000	-937.500	500.000	-0.1333	0.1000	-1250.000	500.000	-0.2667	0.2000	-833.333	500.000	-1.3333	1.0000
500	0.8	-166.66667	-499.999	500.000	-0.3000	0.1000	-625.333	500.000	-0.6000	0.2000	-300.000	1.0000	-1.0000	1.0000
500	0.9	-62.50000	-156.250	500.000	-0.8000	0.1000	-250.000	500.000	-1.6000	0.2000	-80.000	1.0000	-1.0000	1.0000
500	1	0.00000	500.000	500.000	#DIV/0!	0.1000	#DIV/0!	500.000	0.2000	0.2000	#DIV/0!	500.000	1.0000	1.0000

This circuit will be more reasonable to work with. With only 2.5KV to worry about a 3KV cap will work and a single 4.5KV HV16B will be sufficient. Again, the induct currents are low enough that any off the shelf part should do.

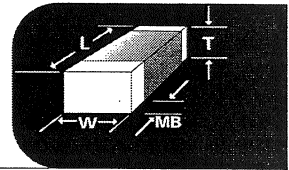
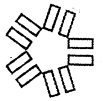
Vect	Duty Cycle	Vg	Road High 5000				Road Med 2500				Road Low 500			
			Vc1	Vc2	I1	I2	Vc1	Vc2	I1	I2	Vc1	Vc2	I1	I2
500	0	500.00000	0.000	500.000	0.000	0.000	500.000	500.000	0.000	0.000	500.000	500.000	0.000	0.000
500	0.1	562.50000	-0.011	625.000	-0.022	0.000	-0.022	625.000	-0.044	0.000	-0.111	625.000	-0.111	0.000
500	0.2	666.66667	-0.025	833.333	-0.050	-0.050	-0.050	833.333	-0.100	-0.050	-0.250	833.333	-0.250	0.000
500	0.3	875.00000	-0.043	1250.000	-0.086	-0.086	-0.086	1250.000	-0.172	-0.086	-0.429	1250.000	-0.429	0.000
500	0.4	1500.00000	-0.067	2500.000	-0.133	-0.133	-0.133	2500.000	-0.267	-0.133	-0.667	2500.000	-0.667	0.000
500	0.5	#DIV/0!	#DIV/0!	2500.000	#DIV/0!	#DIV/0!	#DIV/0!	2500.000	#DIV/0!	#DIV/0!	#DIV/0!	2500.000	#DIV/0!	#DIV/0!
500	0.6	-1000.00000	-0.150	-2500.000	-0.300	-0.300	-0.300	-2500.000	-0.600	-0.300	-1.500	-2500.000	-1.500	0.000
500	0.7	-375.00000	-0.233	-1250.000	-0.467	-0.467	-0.467	-1250.000	-0.933	-0.467	-2.333	-1250.000	-2.333	0.000
500	0.8	-166.66667	-0.400	-833.333	-0.800	-0.800	-0.800	-833.333	-1.600	-0.800	-4.000	-833.333	-4.000	0.000
500	0.9	-62.50000	-0.900	-625.000	-1.800	-1.800	-1.800	-625.000	-3.600	-1.800	-9.000	-625.000	-9.000	0.000
500	1	0.00000	#DIV/0!	500.000	#DIV/0!	#DIV/0!	#DIV/0!	500.000	0.2000	0.2000	#DIV/0!	500.000	1.0000	1.0000

Vout Duty Cycle	Rload High 5000						Rload Med 2500						Rload Low 500					
	Vg	Vc1	Vc2	I11	I12	I12	Vg	Vc1	Vc2	I11	I12	I12	Vg	Vc1	Vc2	I11	I12	
0	50.00000	50.000	50.000	0.0100	0.0100	0.0100	50.00000	50.000	50.000	0.0200	0.0200	0.0200	50.00000	50.000	50.000	0.1000	0.1000	
0.1	56.25000	62.500	50.000	0.0089	0.0100	0.0100	62.50000	50.000	50.000	0.0178	0.0200	0.0200	62.50000	50.000	50.000	0.0889	0.1000	
0.2	66.66667	83.333	50.000	0.0075	0.0100	0.0100	83.33300	50.000	50.000	0.0150	0.0200	0.0200	83.33300	50.000	50.000	0.0750	0.1000	
0.3	87.50000	125.000	50.000	0.0057	0.0100	0.0100	125.00000	50.000	50.000	0.0114	0.0200	0.0200	125.00000	50.000	50.000	0.0571	0.1000	
0.4	150.00000	250.000	50.000	0.0033	0.0100	0.0100	250.00000	50.000	50.000	0.0067	0.0200	0.0200	250.00000	50.000	50.000	0.0333	0.1000	
0.5	#DIV/0!	50.000	50.000	0.0000	0.0100	0.0100	#DIV/0!	50.000	50.000	0.0000	0.0200	0.0200	#DIV/0!	50.000	50.000	0.0000	0.1000	
0.6	-100.00000	-250.000	50.000	-0.0050	0.0100	0.0100	-250.00000	50.000	50.000	-0.0100	0.0200	0.0200	-250.00000	50.000	50.000	-0.0500	0.1000	
0.7	-37.50000	-125.000	50.000	-0.0133	0.0100	0.0100	-125.00000	50.000	50.000	-0.0267	0.0200	0.0200	-125.00000	50.000	50.000	-0.1333	0.1000	
0.8	-16.66667	-83.333	50.000	-0.0300	0.0100	0.0100	-83.33300	50.000	50.000	-0.0600	0.0200	0.0200	-83.33300	50.000	50.000	-0.3000	0.1000	
0.9	-6.25000	-62.500	50.000	-0.0800	0.0100	0.0100	-62.50000	50.000	50.000	-0.1600	0.0200	0.0200	-62.50000	50.000	50.000	-0.8000	0.1000	
1	0.00000	50.000	50.000	#DIV/0!	0.0100	0.0100	#DIV/0!	50.000	50.000	#DIV/0!	0.0200	0.0200	#DIV/0!	50.000	50.000	#DIV/0!	0.1000	

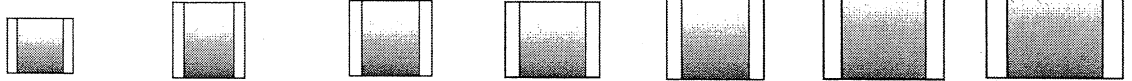
with these low voltages, we can use a 500V cap and a smaller DEI, DE-375 power mosfet which has a low RDS but can still standoff 300V.

Vout Duty Cycle	Rload High 5000						Rload Med 2500						Rload Low 500					
	Vg	Vc1	Vc2	I11	I12	I12	Vg	Vc1	Vc2	I11	I12	I12	Vg	Vc1	Vc2	I11	I12	
0	50.00000	50.000	50.000	0.0000	0.0100	0.0100	50.00000	50.000	50.000	0.0000	0.0200	0.0200	50.00000	50.000	50.000	0.0000	0.1000	
0.1	56.25000	62.500	50.000	-0.0011	0.0100	0.0100	62.50000	50.000	50.000	-0.0022	0.0200	0.0200	62.50000	50.000	50.000	-0.0111	0.1000	
0.2	66.66667	83.333	50.000	-0.0033	0.0100	0.0100	83.33300	50.000	50.000	-0.0055	0.0200	0.0200	83.33300	50.000	50.000	-0.0225	0.1000	
0.3	87.50000	125.000	50.000	-0.0044	0.0100	0.0100	125.00000	50.000	50.000	-0.0099	0.0200	0.0200	125.00000	50.000	50.000	-0.0443	0.1000	
0.4	150.00000	250.000	50.000	-0.0077	0.0100	0.0100	250.00000	50.000	50.000	-0.0133	0.0200	0.0200	250.00000	50.000	50.000	-0.0677	0.1000	
0.5	#DIV/0!	50.000	50.000	0.0000	0.0100	0.0100	#DIV/0!	50.000	50.000	0.0000	0.0200	0.0200	#DIV/0!	50.000	50.000	0.0000	0.1000	
0.6	-100.00000	-250.000	50.000	-0.0150	0.0100	0.0100	-250.00000	50.000	50.000	-0.0300	0.0200	0.0200	-250.00000	50.000	50.000	-0.1500	0.1000	
0.7	-37.50000	-125.000	50.000	-0.0223	0.0100	0.0100	-125.00000	50.000	50.000	-0.0447	0.0200	0.0200	-125.00000	50.000	50.000	-0.2233	0.1000	
0.8	-16.66667	-83.333	50.000	-0.0400	0.0100	0.0100	-83.33300	50.000	50.000	-0.0800	0.0200	0.0200	-83.33300	50.000	50.000	-0.4000	0.1000	
0.9	-6.25000	-62.500	50.000	-0.0900	0.0100	0.0100	-62.50000	50.000	50.000	-0.1800	0.0200	0.0200	-62.50000	50.000	50.000	-0.9000	0.1000	
1	0.00000	50.000	50.000	#DIV/0!	0.0100	0.0100	#DIV/0!	50.000	50.000	#DIV/0!	0.0200	0.0200	#DIV/0!	50.000	50.000	#DIV/0!	0.1000	

HIGH VOLTAGE MLC - COMMERCIAL



COMMERCIAL RANGES, RATED TO 10 KV



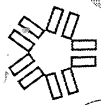
SIZE	3530	4040	4540	5440	5550	6560	7565
Min Cap(C0G/X7R)	390/102	390/102	390/102	390/102	390/102	560/202	101/202
LENGTH L	.350 (8.89)	.400 (10.2)	.450 (11.4)	.540 (13.7)	.550 (11.4)	.650 (16.5)	.750 (19.0)
WIDTH W	.300 (7.62)	.400 (10.2)	.400 (10.2)	.400 (10.2)	.500 (12.7)	.600 (15.2)	.650 (16.5)
T MAX	.250 (6.35)	.300 (7.62)	.300 (7.62)	.300 (7.62)	.300 (7.62)	.300 (7.62)	.400 (10.2)
MB	.050 (1.27)	.060 (1.52)	.060 (1.52)	.060 (1.52)	.060 (1.52)	.060 (1.52)	.060 (1.52)

MAXIMUM CAPACITANCE

3 Digit Code; See How to Order

VOLTAGE	C0G		X7R		C0G		X7R		C0G		X7R		C0G		X7R	
	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0
500V	683	105	473	125	104	155	563	225	154	275	224	475	274	685		
1000V	223	474	393	824	473	105	563	105	823	155	124	225	154	275		
2000V	153	683	273	154	333	184	393	184	563	274	823	394	104	684		
3000V	123	333	223	683	273	823	273	104	393	154	683	224	823	394		
4000V	682	183	153	333	183	393	223	473	333	683	473	104	563	184		
5000V	272	103	472	183	562	223	682	273	103	393	123	563	223	104		
6000V	152	562	272	123	272	123	332	153	472	223	682	333	103	563		
7000V	122	392	182	822	222	103	272	123	392	153	562	223	822	393		
8000V	102	332	182	682	182	682	222	822	332	123	472	183	682	333		
9000V	821	272	152	562	182	562	222	682	332	103	332	153	562	273		
10000V	471	222	152	472	821	472	102	562	152	822	222	123	562	223		

Dimensions are in inches, bracketed dimensions in millimeters. Tolerances are +/- 5% L & W, or .015" (0.38 mm), whichever is greater. MB dimensions are maximum.



HIGH VOLTAGE MLC - COMMERCIAL



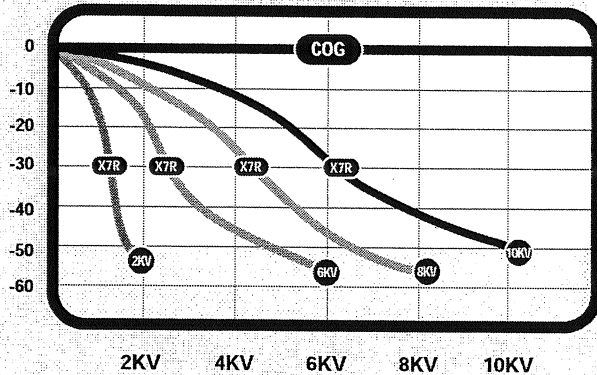
COG DIELECTRIC CHARACTERISTICS

OPERATING TEMPERATURE RANGE: -55°C to 125°C
 TEMPERATURE COEFFICIENT: 0 +/- 30 ppm/°C
 DISSIPATION FACTOR: .001 (0.1%) max @ 25°C
 INSULATION RESISTANCE, 25°C: >100GΩ or >1000ΩF
 125°C: >10GΩ or >100 F
 DIELECTRIC WITHSTANDING VOLTAGE: 120% VDCW, or 750V*
 *WHICHEVER IS GREATER
 AGING RATE: 0% per decade
 TEST PARAMETERS: 1KHz, 1.0 +/- 0.2 VRMS, 25°C
 1MHZ for Capacitance <100pF

X7R DIELECTRIC CHARACTERISTICS

OPERATING TEMPERATURE RANGE: -55°C to 125°C
 TEMPERATURE COEFFICIENT: +/-15% ΔC Max.
 DISSIPATION FACTOR @ 25° C: .025 (2.5%) max @ 25°C
 INSULATION RESISTANCE, 25°C: >100GΩ or >1000ΩF
 125°C: >10GΩ or >100ΩF
 DIELECTRIC WITHSTANDING VOLTAGE: 120% VDCW, or 750V*
 *WHICHEVER IS GREATER
 AGING RATE: < 2.0% per decade
 TEST PARAMETERS: 1KHz, 1.0 +/- 0.2 VRMS, 25°C

%ΔC TYPICAL VOLTAGE COEFFICIENT



Dielectric withstanding voltage testing requires immersion of the device in a dielectric fluid to preclude arcing over the chip surface, notably at voltages exceeding 1000 VDC. Conformal coating of chips is recommended in use to eliminate arcing.

HOW TO ORDER

4540	B	103	M	302	N	X	T	M
SIZE See Chart	DIELECTRIC N = COG B = X7R	CAPACITANCE Value in Picofarads Two significant figures, followed by number of zeros: 103 = 10,000 pF	TOLERANCE J = +/- 5 % K = +/- 10 % M = +/- 20 % Z = +80%-20% P = +100%-0%	VOLTAGE-VDCW Two significant figures, followed by number of zeros: 302= 3000V	TERMINATION N = Nickel Barrier (100% Sn) sizes 1515 to 4540 only P = Palladium Silver Y = Nickel Barrier (90Sn/10Pb) sizes 1515 to 4540 only	THICKNESS OPTION X = Non-standard thickness. Specify in Mils if non-standard is required. Standard items are any thickness to Max shown in charts.	PACKING OPTION T = Reeled	MARKING OPTION M = Marked (See Marking Specifications)

