Lecture 48 Review of Feedback

HW # 4 Erickson Problems Ch. 9 #'s 7 &9 and questions in lectures

I. Review of Negative Feedback

- A. General
 - 1. Overview
 - 2. Summary of Advantages
 - 3. Disadvantages
- B. Buck Converter Example
 - 1. Open Loop Transfer Function

a.
$$G_{vd}(s) = V_o(s)/\hat{d}(s)$$

b.
$$G_{vg}(s) = V_o(s)/V_g(s)$$

c.
$$Z_o(s)$$

2. Closed Loop Around $V_o \& d$

$$\begin{bmatrix} \frac{V_{out}}{V_{ref}} \end{bmatrix}_{CL} = \frac{1}{H} \frac{T}{1+T}, \ [Z_o]_{CL} = \frac{Z_o}{1+T} \\ \begin{bmatrix} \frac{V_{out}}{V_g} \end{bmatrix}_{CL} = \frac{G_{vg}(s)}{1+T(s)} \\ 3. \quad T(s), \ \frac{T(s)}{1+T(s)} \text{ and } \frac{1}{1+T(s)} \\ a. \quad T(s) \text{ from Laplace Transform} \\ b. \quad T(f) \text{ vs f in db units} \\ \end{bmatrix}$$

c. $\frac{T}{1+T}$ from T via Algebra on graph

d. $\frac{1}{1+T}$ plots from T via Algebra on graph e. $\left[\frac{V_{out}}{V_g}\right]_{CL} \equiv \frac{G_{vd}}{1+T(s)}$ by Algebra or graph

4. Basic Feedback via Pbm 9.2

Lecture 48 Review of Feedback

I. Review of Negative Feedback

A. General Issues

1. Overview

We have a switch mode converter, which we wish to stabilize from load and input changes by employing feedback to maintain the output level to a pre-established level.

> Objective: maintain constant output voltage r(t) = V, in spite of disturbances in $v_{g}(t)$ and $i_{trad}(t)$.

Typical variation in $r_s(r)$: 100Hz or 120Hz ripple, produced by rectifier circuit.



Load current variations: a significant step-change in load current, such as from 50% to 100% of rated value, may be applied.

A typical output voltage regulation specification: $5V \pm 0.1V$.

Circuit elements are constructed to some specified tolerance. In high volume manufacturing of converters, all output voltages must meet specifications.

To implement this feedback we need to sense the output and compare it to a reference value to provide a difference signal, which drives the pulse width modulator to provide the proper value of duty cycle to drive the difference to zero. This is shown on the top of page 4. Note also the use of a compensator-box, which is employed to tailor the open loop response so that when feedback is used to reduce the effect of sudden load and input changes, no oscillation occurs. This use of feedback is artful because the power supply response is slow and if the feedback acts too fast the system may oscillate. The art is to tailor the response of the error amplifier, via the compensator-box components.



Later we will represent each box above by a transfer function, which will both amplitude and phase plots versus frequency called Bode plots. The Bode plot employs logarithms so that when we have a tandem series of transfer functions the combined response is just the sum of the individual Bode responses. In block diagram form we would have a feedback system as shown below.



The block diagram has three components inside the dashed line box on the right. Note that both line voltage variations and load current variations lie outside the feedback loop. Duty cycle variations lie within the voltage feedback loop. We can derive a

solution to the block diagram output voltage that includes contributions from V_G , I_{load} and Z_{out} as shown below.

Manipulate block diagram to solve for $\hat{i}(s)$. Result is

$$\hat{r} = \hat{r}_{ref} \frac{G_{c}G_{rd} / V_{M}}{1 + HG_{c}G_{rd} / V_{M}} + \hat{v}_{g} \frac{G_{rg}}{1 + HG_{c}G_{rd} / V_{M}} \pm \hat{i}_{load} \frac{Z_{out}}{1 + HG_{c}G_{rd} / V_{M}}$$

which is of the form

$$\hat{v} = \hat{v}_{ref} \frac{1}{H} \frac{7}{1+T} + \hat{v}_g \frac{G_{rg}}{1+T} \pm \hat{i}_{load} \frac{Z_{out}}{1+T}$$

with
$$T(s) = H(s) G_c(s) G_{ra}(s) / V_M = "loop gain"$$

Loop gain T(s) = products of the gains around the negative feedback loop.

Each of block components determines T(s). While we can easily identify the LOOP GAIN, T(s), for the converter system, we now ask how does it respond to disturbances? And how does feedback on the converter vary as the magnitude of T(s) varies with frequency? Are all frequencies the same for feedback?

Original (open-loop) line-to-output transfer function:

$$G_{ig}(s) = \frac{\hat{r}(s)}{\hat{\nu}_{g}(s)} \bigg|_{\substack{\hat{d}'=0\\\hat{i}_{load}=}}$$

With addition of negative feedback, the line-to-output transfer function becomes:

$$\frac{\hat{f}(s)}{\hat{f}'_{g}(s)}\Big|_{\substack{\hat{f}_{ref}=0\\\hat{f}_{food}=0}} = \frac{G_{rg}(s)}{1+Z(s)}$$

Feedback reduces the line-to-output transfer function by a factor of

$$\frac{1}{1+\overline{7(s)}}$$

If $\mathcal{T}(s)$ is large in magnitude, then the line-to-output transfer function becomes small.

We have to insure that T(s) is large at frequencies of interest. This is most readily accomplished by employing a high(<10³) gain operational amplifier in the feedback loop. Since line frequencies are around 50-0 Hertz and harmonics the DC gain is the determining factor to reduce line frequency variations. What about changes in the output due to load current variations?

Original (open-loop) output impedance:

$$Z_{cond}(s) = \pm \frac{\hat{i}(s)}{\hat{i}_{load}(s)} \bigg|_{\substack{\hat{d}=0\\\hat{i}_{g}=0}}$$

With addition of negative feedback, the output impedance becomes:

$$\frac{\hat{i}(s)}{\pm \hat{i}_{load}(s)} \bigg|_{\substack{\hat{r}_{ref}=0\\ \hat{r}_{g}=0}} = \frac{Z_{out}(s)}{1 + \mathcal{T}(s)}$$

Feedback reduces the output impedance by a factor of

$$\frac{1}{1+\mathcal{T}(s)}$$

If 77/3/is large in magnitude, then the output impedance is greatly reduced in magnitude.

Here we want T(s) to be large at high frequencies, not just at DC. This insures that the high frequency output impedance with feedback is much less that the open loop output impedance. What about the closed loop reference voltage to output voltage transfer function? In short Z_{OUT} varies with loop gain.

$$\frac{\hat{r}(s)}{\hat{r}_{ref}(s)}\bigg|_{\substack{\hat{r}_{g}=0\\\hat{r}_{loosf}=0}} = \frac{1}{H(s)} \frac{Z(s)}{1+Z(s)}$$

If the loop gain is large in magnitude, i.e., $|| \mathcal{T} || >> 1$, then $(1+T) \approx T$ and $T/(1+T) \approx T/T = 1$. The transfer function then becomes

$$\frac{\hat{\imath}(s)}{\hat{\imath}_{rej}(s)}\approx\frac{1}{H(s)}$$

which is independent of the gains in the forward path of the loop.

This result applies equally well to dc values:

$$\frac{V}{V_{rr'}} = \frac{1}{\mathcal{H}(0)} \frac{\mathcal{T}(0)}{1 + \mathcal{T}(0)} \approx \frac{1}{\mathcal{H}(0)}$$

That is the output of the converter is some fixed ratio of the reference voltage, if T(s) is LARGE. We can insure this from DC to high frequency by tailoring loop gain T(s). A typical T(s) and its amplitude Bode plot versus frequency is shown in the middle of page 7. All the benefits of feedback only occur when T(s) is LARGE. Since T(s)= H(s) $G_C(s) G_{VD}(s) / V_M$. We normally rely on a large value of H(s) from the sensor gain block, which often includes an operational amplifier and a compensation network to tailor the loop gain. The H(s) block is in the low power portion of the loop and easy to work with. H(s) often possesses several isolated poles and several isolated zeros. The $G_{VD}(s)$ block is the control duty cycle to output voltage transfer function and usually possesses two poles at the same frequency, ω_{P1} . All togeather the loop gain would contain three poles and one zero as shown below If H(s) had but one pole and one zero.



When the loop gain goes to unity all the benefits of feedback are lost. This has implications as discussed below.

$$\frac{T}{1+T} \approx \begin{cases} 1 & \text{for } || T || \gg 1\\ T & \text{for } || T || \ll 1 \end{cases}$$
$$\frac{1}{1+T(s)} \approx \begin{cases} \frac{1}{T(s)} & \text{for } || T || \gg 1\\ 1 & \text{for } || T || \ll 1 \end{cases}$$

At frequencies sufficiently less that the crossover frequency, the loop gain $\mathcal{T}(\mathcal{A})$ has large magnitude. The transfer function from the reference to the output becomes

$$\frac{\hat{\nu}(s)}{\hat{\nu}_{ref}(s)} = \frac{1}{H(s)} \frac{Z(s)}{1 + Z(s)} \approx \frac{1}{H(s)}$$

This is the desired behavior: the output follows the reference according to the ideal gain $1/\mathcal{H}(s)$. The feedback loop works well at frequencies where the loop gain $\mathcal{I}(s)$ has large magnitude.

At frequencies above the crossover frequency, $|| \mathcal{I} || < 1$. The quantity $\mathcal{I}(1+\mathcal{I})$ then has magnitude approximately equal to 1, and we obtain

$$\frac{\hat{r}(s)}{\hat{r}_{rel}(s)} = \frac{1}{H(s)} \frac{T(s)}{1 + T(s)} \approx \frac{T(s)}{H(s)} = \frac{G_c(s)G_{rel}(s)}{V_M}$$

This coincides with the open-loop transfer function from the reference to the output. At frequencies where $\| \mathcal{T} \| < 1$, the loop has essentially no effect on the transfer function from the reference to the output.

It also has effects on how the feedback loop effects the line voltage and output current disturbances.

Below the crossover frequency: $f < f_c$ and || Z || > 1

> Then 1/(1+Z) ≈ 1/Z; and disturbances are reduced in magnitude by 1/I ZII

$$\frac{1}{1+\mathcal{T}(s)} \approx \begin{cases} \frac{1}{\mathcal{T}(s)} & \text{for } \|\mathcal{T}\| >> 1\\ 1 & \text{for } \|\mathcal{T}\| << 1 \end{cases}$$

Above the crossover frequency: $f > f_c$ and $|| \mathcal{T} || < 1$

> Then $1/(1+7) \approx 1$, and the feedback loop has essentially no effect on disturbances



$$\frac{\mathbf{V}_{\mathbf{o}}}{\mathbf{V}_{\mathbf{in}}} = \frac{\mathbf{A}_{\mathbf{0}}\mathbf{L}}{1+\boldsymbol{b}\,\mathbf{A}_{\mathbf{0}}\mathbf{L}} \approx \frac{\mathbf{I}}{\boldsymbol{b}}$$

- 1. Advantages
- V_o variations due to A_{0L} variations are eliminated
- V_o variations due to L, R & C "tolerances" are reduced for elements inside A_{0L}
- V_o changes due to power supply variations are reduced

by $\frac{1}{1 + A_{0L}\boldsymbol{b}}$

 Z_o reduced by 1 + $A_{0L}\beta$ therefore I_o variations, less $\Delta V_o~\cdot~Z_{in}$ increased by 1 + $A_{0L}\beta$

V_o changes due to switch changes over time and temp.

3. Feedback Oscillation and Compensation

After we close the feedback loop, sometimes,

oscillation/instability occurs for frequencies where $A\beta = 1 < 180^{\circ}$ or NEARBY this condition. To be safe from undesired oscillation the rule of thumb we will develop later will be:

If $A\beta$ = 1 we require $<A\beta$ be 76° from 180°.

To achieve the proper phase shift at unity gain for T(s), we employ a f compensation element inside the feedback loop. This is only a nominal additional expense - it can be done in low power sections.



The COMPENSATOR box will contain four possible transfer functions listed in the table below to tailor the LOOP GAIN to achieve both **load regulation and transient response**.

Compensation Type	Load Regulation	Transient Response
Single-pole	Good	Poor
Single-pole with in-band gain limiting	Fair	Good
Pole-zero	Good	Good
2-pole-2-zero	Good	Good

Clearly to achieve both goals, pole-zero combinations are best.

The subject of closed loop stability will be given in detail in Lecture 49, but below we briefly outline the major issues in anticipation. We define phase margin, gain margin and excess phase in the figure below, which contains both amplitude and



phase plots of the OPEN LOOP gain, T(s).

Changes in gain, Δ G, and changes in phase $\Delta \phi$ along a gain slope are shown for a 20 db per decade slope in the figure below. We will often have to be quantitative about gain and phase changes as we move along gain slopes from one frequency to another. In particular we will add COMPENSATION to the open loop gain via the compensation block to get the proper amplitude and phase plots in order to achieve **the two goals of feedback both improved load regulation and improved transient response.** This is artful, as we must trade one benefit off against another. Feedback Loop Compensation



Figure B-8 Illustrating how the mathematical tools are used.

Remember that the compensation box is in series in the loop gain so it is able to modify and tailor both the amplitude and phase responses of T(s) individually. Phase shifts generally kick in at 1/10 the break frequencies for amplitude changes and persist till 10 times the break frequency for amplitude changes. To quantify both phase differences and amplitude differences at changing frequencies in compensation networks we show two rules of thumb below.

-20 db gain slopes have:

1. $\Delta G(f_2-f_1) = 20 \log (f_2/f_1)$

2.
$$\Delta \phi(f_2 - f_1) = \tan^{-1}(f_2/f_1)$$

-40 db gain slopes have:

- **1.** $\Delta G(f_2-f_1) = 40 \log (f_2/f_1)$
- **2.** $\Delta \phi(f_2 f_1) = 2 \tan^{-1}(f_2 / f_1)$

What a proper amplitude and phase plot for the open loop gain to achieve both **improved load regulation and improved transient response** will be given later in Lecture 49.

SHOW for HW # 4 that the four op amp circuits below provide the following: (a) a single pole filter, (b) a single pole with gain limiting for achieving both flat high and low frequency response, (c) a single pole/zero combination and (d) a two pole/two zero combination. **Draw both the gain and phase plots for all four circuits indicating the location of all poles and zeros**. Use you junior electronics to solve each op amp circuit.



B. Buck Converter Example

1. **Open Loop**: Three independent inputs: \hat{d} , \hat{V}_g , \hat{i}_{liter}



2. Closed Feedback Loop around $V_o \& V_{ref}$

Compare V_o to V_{ref} to drive the duty cycle to drive V_o \equiv V_{ref} Duty cycle control is "the old way." Modern converters use the "current programmed mode" described in chapter11 of Erickson in addition to the voltage feedback loop. Still we start here, with a simple voltage feedback loop. The system is as shown below: Open loop w.r.t. i_{load} and V_g



Closed loop w.r.t. (V_o, V_{ref}) via \hat{d} control and the pulse width modulator loop, ultimately ΔV_g , ΔV_{ref} and Δi_{load} will cause ΔV_{out} variation. In the closed feedback loop we find:

$$T \equiv \text{loop gain} = \frac{HG_cG_{vd}}{V_D} = T(f)$$

$$\downarrow$$

varies with frequency



 \Rightarrow say 2V 2nd harmonic @120Hz on V_g only causes 2 mV change in V_o with the loop closed.

 $\frac{\Delta V_o}{\Delta i_{out}}$ is also reduced by 1000 \Rightarrow Z_o with feedback is effectively 1000 times smaller, when we achieve T= 1000 over a

effectively 1000 times smaller, when we achieve T=1000 over a limited frequency range.

 $\frac{\hat{V}_{out}}{\hat{V}_{ref}} = \frac{1}{H} \Rightarrow \text{For stable output use(frequency/temperature)} \\ \text{compensated resistors etc. 0.1\% resistor is only 50} \\ \text{cents}$

3. Frequency Response of the Feedback Loop There are three parts: Loop Gain T(s),

$$T(s)/1+T(s) \& \frac{1}{1+T(s)}$$

a. Consider only T(s) with three Poles and One Zero

$$T(s) = \frac{T_o \ 1 + S/W_z}{\left[1 + \frac{s}{QW_p 1} + (\frac{s}{W_{p1}^2})\right] \left[1 + s/W_{p2}\right]}$$

Use low Q approximation to solve for well separated poles or quadratic equation for close roots. If in cubic form just employ a root solver - like on a HP 48 calculator, to get all three poles.

b. Draw Amplitude of T(s) in db vs frequency: T(f)Use log-log plots, then express all amplitudes in db. We can from pole and zero locations draw both T and T/1+T by hand to a first approximation.



c. T/1+T comes easily from T via Algebra on graph

T large:
$$\frac{T}{1+T} \rightarrow 1$$
 or "0" db
T small: $\frac{T}{1+T} \rightarrow T$

 \Rightarrow Easier to do on graph than using algebra! Asymptote with a double pole will have a "Q peak" as we saw on page 7. This "Q peak" will have no effect on T/1+T plots since when T is very large T/1+T \rightarrow 1 anyway. However this "Q peaking of T(s)" will effect other plots such as $\frac{G_{vg}}{1+T}$,

 $\frac{Z_0}{1+T}$ as we will see below.

Consider how to plot 1/1+T from T d.



Original transfer functions, before introduction of feedback ("open-loop transfer functions"):

 $G_{\nu}(s) = G_{\nu}(s) = Z_{out}(s)$

Upon introduction of feedback, these transfer functions become ("closed-loop transfer functions"):

$$\frac{1}{H(s)} \frac{\mathcal{I}(s)}{1 + \mathcal{I}(s)} = \frac{G_{rg}(s)}{1 + \mathcal{I}(s)} = \frac{Z_{out}(s)}{1 + \mathcal{I}(s)}$$

The loop gain:

TYs)

Next we include the "Q peaking of T(s)" in T and 1/(1+T)



Given $\frac{1}{1+T}$ plots versus frequency lets see its effect on a

typical open loop $G_{VG}(s)$ factor when we close a feedback loop around $G_{VG}(s)$. The closed loop converter characteristics are found in a three-step process. First plot the 1/(1+T) characteristic versus frequency, from known T versus frequency plots by algebra on the graph. Next plot the open loop $G_{VG}(f)$ characteristics, which have a simple double pole at the same frequency break point. Finally combine the product of the two terms togeather by algebra on the graph.

From DC to the location of the double pole the product is just G_{VG} divided by 1/(1+T). Since above the double pole location , f_P , G_{VG} falls at 40 db per decade while 1/(1+T) rises at 40 db per decade the product is constant. When the zero at f_Z kick's in in 1/(1+T) the product falls off at 20 db per decade all the way to the crossover frequency of G_{VG} when the product is in the negative db region. See plots below.



4. Let's next consider a simple example. Basic feedback conditions are shown in a complex system such as that of Pbm. 9.2 from Erickson. See page 19

The flyback convener system of Fig. 9.4) contains a feedback loop for regulation of the main output voltage v_i . An auxiliary output v_i is produces voltage v_i . The do input voltage v_i lies in the range 280V $\leq v_i \leq 380$ V. The compensator perwork has granter function.

$$G_{q}(s) = G_{qq}\left(1 + \frac{\omega_{1}}{s}\right)$$

where $G_{r+} = 0.05$, and $f_1 = \omega_0/2\pi = 400$ Hz.

- (a) What is the steady-state value of the error voltage v_i(t)ⁿ Explain your reasoning.
- (b) Determine the steady-state value of the main output voltage v_i.
- (c) Estimate the steady-state value of the auxiliary output voltage v_r.





a. V(error) should ideally be zero! $\Rightarrow G_c(f=0) \rightarrow \infty$ b. $\frac{V_{out}}{V_{ref}} = \frac{1}{H(s)} \frac{T(s)}{1+T(s)} \rightarrow \frac{1}{H(s)} \text{ for } T \rightarrow \infty$ $V_{out} = \frac{V_{ref}}{H(s)} = \frac{3}{1/5} = 15V$

c.
$$V_2 \text{ out} = ?$$
 $n:n_1$ $n:n_2$
 $V_1 = 15V$ with 64:3 Trf all else the same for 64:1
 $V_2 = 1/3 V_1 = 5V$