## Lecture 47

# Switch Mode Converter Transfer Functions: $T_{vd}(s)$ and $T_{vg}(s)$

## A. Guesstimating Roots of Complex Polynomials( this section is optional)

- 1. Quick Insight n=2
- 2. n<sup>th</sup> order case
- 3. Cuk example
- 4. Forth Order Filter Problem 8.19

## B. Buck Boost $T_{vd}(s)$ and $T_{vg}(s)$ Analysis

- 1. Circuit Model
- 2. Loop Equations
- 3.  $T_{vg}(s)$  in standard form : Two poles
- 4.  $T_{vd}(s)$  in standard form : RHP Zero and two poles
- 5. Specific Buck-Boost Design
  - a. DC conditions : CCM
  - b. AC Bode plots : CCM

## C. Right Half Plane Zeros

- 1. Transient Response
- 2. Origin of RH plane Zero in differing delays
- 3. T(s) of a single Zero insight
- 4. Phase Margin Limitations with RH Plane Zero

- A. Guesstimating Roots of Complex Polynomials : P(s)
- 1. Quick Insight

$$a_2 s^2 + a_1 s + 1 = (1 + a_1 s)(1 + \frac{a_2}{a_1} s)$$
  
=  $1 + s[a_1 + \frac{a_2}{a_1}] + a_2 s^2$ 

OK approximation only if

 $\frac{a_2}{a_1} << a_1$ 

Why bother? Left Side Roots hard to <u>guess</u>

Right Side Roots are easy to guess

$$s_1 = -\frac{1}{a_1}$$
 low f root coeff. if  $a_1$  is big  

$$s_2 = -\frac{a_1}{a_2}$$
 high f root if  $a_1$  is big

This is the Widely separated root approximation:

Can we generalize this to higher order polynomials? Can we avoid root finder programs? It's really academic as root finders are widely available on calculators.

2.  $n^{th}$  order case a. ideal case for approximation  $R(s) = 1 + a_1s + a_2s^2 + a_ns^n$  All a's are real Form is arbitrary but USEFUL

Beat/Cajole/Force into standard form

 $R(s) = (1 + t_1 s)(1 + t_2 s) \dots (1 + t_n s) \rightarrow easy to see by expanding out$ all the factors. t's or time constants are either real or complex  $a_1 = t_1 + t_2 + \dots + t_n$  $a_2 = t_1(t_2 + t_3 + ... t_n) + t_2(t_3 + t_4 ... t_n)$ 

$$a_{3} = t_{1}t_{2}(t_{3}+t_{4}+...t_{n}) + t_{2}t_{3}(t_{4}+t_{5}+...t_{n})$$

 $a_n = t_1 t_2 t_3 \dots t_n \longrightarrow$  easy to see by expanding out factors. We ask under what conditions can solutions be easily **approximated?** We can use above for approximating roots if the roots are real and well seperated. Then only the first term of each equation is dominant.

$$|t_1| > |t_2| > |t_3| > ... > |t_n|$$

This is a **big** if and the goal is to be clever in choosing the t's in descending order when one does the factoring by hand of polynomials. Few cases are ideal.

If successful,

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 $a_1 = t_1 \xrightarrow{} t_1 = a_1$ 

 $a_2 = t_1 t_2 \xrightarrow{} t_2 = a_2/a_1$  } Works for simple second order insight case

$$a_3 = t_1 t_2 t_3 \xrightarrow{} t_3 = a_3/a_2$$

$$a_n = t_1 t_2 \quad t_n \dashrightarrow t_n = a_n / a_{n-1}$$
 In summary this assumes no inequalities are violated.

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P(s) must be in this standard form then the polynomial can be approximately factored by :

$$P(s) = (1 + a_1 s) (1 + \frac{a_2}{a_1} s) (1 + \frac{a_3}{a_1} s)$$

$$\downarrow \qquad \downarrow \qquad \downarrow \qquad \downarrow$$
lowest f second f third highest f
pole pole pole

b. What if one inequality is not met

ideal: 
$$P(s) = (1 + a_1 s)(1 + \frac{a_2}{a_1} s) \dots (1 + \frac{a_n}{n-1} s)$$

 $|a_1|\rangle\rangle |\frac{a_2}{a_1}|\rangle\rangle |\frac{a_3}{a_2}|\rangle\rangle...\rangle\rangle |\frac{a_n}{a_{n-1}}|$ 

Non-ideal case where one inequality is not satisfied we leave the corresponding roots in quadratic form:

$$\frac{a_k}{a_{k-1}} \nearrow \frac{a_{k+1}}{a_k}$$

$$P(s) = (1+a_1s)(1+\frac{a_2}{a_1}s)..\left[1+\frac{a_k}{a_{k-1}}s+\frac{a_{k+1}}{a_{k-1}}s\right]$$

$$\bullet \dots (1+\frac{a_n}{a_{n-1}})$$
The quadratic equation below

The quadratic equation below

$$\begin{bmatrix} 1 + \frac{a_k}{a_{k-1}}s + \frac{a_{k+1}}{a_{k-1}}s^2 \end{bmatrix}$$

$$\downarrow \qquad \qquad \downarrow$$

we solve as a usual quadratic Valid only forthis pair of roots only  $a_k > a_{k-1} * a_{k+1}$ 

When several inequalities are violated then we must use cubic or higher order roots and often the roots are close in value.

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**c.** Cubic Polynomial: In general for a cubic polynomial this may occur two ways1 +  $a_1s + a_2s^2 + a_3s^3$ 

1. First and Second Close Roots Case

First guess  $(1+a_1s)(1+\frac{a_2}{a_1}s)(1+\frac{a_3}{a_2}s)$ a2 ---X-----X------X------X------S<sub>1</sub>  $S_2$ S<sub>3</sub> close pair isolated root  $(1+a_1s)(1+\frac{a_2}{s}s)$  $(1 + \frac{a_3}{s})$ a2 **a**1 still valid solution not valid approximation  $s_3 = -\frac{a_2}{a_3}$ Use:  $(1 + a_1s + a_2s^2)$ aз to solve for unknown  $s_1$  and  $s_2$  just solve quadratic quadratic validity  $(1+a_1s)(1+\frac{a_2}{a_1}s)$  $a_k > a_{k-1} a_{k+1}$  $1 + (a_1 + \frac{a_2}{a_1})s + a_2s^2$ k = 1  $a_1 > \frac{a_2}{a_1} - a_1^2 > a_0^1 a_2$  $a_1^2 > a_2$ Summary:  $1 + \frac{a_3}{s}$  $1 + a_1s + a_2s^2$ **a**2 ↓ ↓  $s_1$  and  $s_2$ S<sub>3</sub> 2. Second and Third Roots Close Case ---X-----X-----X-----X----- $S_1$ **S**<sub>2</sub> S<sub>3</sub>

Original equation  $1 + a_1s + a_2s^2 + a_3s^3$  $(1 + \frac{a_2}{s}s) \quad (1 + \frac{a_3}{s}s)$ (1+a₁s) Isolated root Invalid First guess valid approximation Close roots Not valid  $(1+\frac{a_2}{a_1}s)(1+\frac{a_3}{a_2}s)$ Use:  $1 + (\frac{a_2}{a_1})s + (\frac{a_3}{a_1})s^2$  $1 + (\frac{a_2}{a_3} + \frac{a_3}{a_3})s + \frac{a_3}{a_3}s^2$ Use this quadratic to solve for s<sub>2</sub> a1 a2 a1 and  $s_3$  only if provided  $\frac{a_3}{4}$  ( $\langle \frac{a_2}{4} \rangle$  $a_k^2 > a_{k-1} a_{k+1}$  $a_2 a_1$  $a_2^2 > a_1 a_3$ k = 2  $a_2^2 > a_1 a_3$ Summary:  $1+\frac{a_2}{a_1}s+ \ \frac{a_3}{a_1}s^2$  $(1 + a_1s)$  $s_1 = -1/a_1$ yields  $s_2$  and  $s_3$ 

#### 3. Cubic Polynomial Case for CuK

CuK converter with D, D' left out for simplicity

How to factor D(s) depends on circuit values. We want (but not always valid)

$$D(s) = (1+a_1s)(1+\frac{a_2}{a_1}s)(1+\frac{a_3}{a_2}s)$$

$$\mathsf{T}(\mathsf{s}) = \left[1 + \operatorname{s}(\frac{L_1 + L_2}{R})\right] \left[1 + \operatorname{s}\frac{\operatorname{RC} L_1}{L_1 L_2}\right] \left[1 + \operatorname{s}\frac{L_2}{R}\right]$$

a.  $a_1 > \frac{a_2}{a_1} > \frac{a_3}{a_2}$  ideal case of well separated roots If L<sub>1</sub> is chosen >> L<sub>2</sub> the approximate factorization is OK because:  $\frac{L_1}{R} > RC > \frac{L_2}{R}$  for the ideal case of well separated roots  $\left[1+s\frac{L_1}{R}\right]\left[1+sRC\right]\left[1+s\frac{L_2}{R}\right] \rightarrow easy to get roots from circuit$ elements. Consider D(s) =  $(1 + a_1s)(1 + \frac{a_2}{a_1}s)(1 + \frac{a_3}{a_2}s)$ Whereas if no approximations are made:  $\left[1+s(\frac{L_1+L_2}{R})\right]\left[1+s\frac{RCL_1}{L_1+L_2}\right]\left[1+s\frac{L_2}{R}\right]$ What if you find the second inequality is violated  $a_1$   $\rangle\rangle$   $\frac{a_2}{a_1} \neq \frac{a_3}{a_2}$ due to component choices!

 $\frac{L_1 + L_2}{R} \gg RC \ \frac{L_1}{L_1 + L_2} \ \text{but not} \ \ \rangle \rangle \frac{L_2}{R}$ 

This will require a different approach.

b. Second and Third Terms Roots Close

Then for easy root solving we break T(s) into two terms: one with a single real root and the second with two possibly complex roots. Still valid Use this Quadratic

$$\left[1 + s(\frac{L_1 + L_2}{R})\right] \qquad \left[1 + sRC\frac{L_1}{L_1L_2} + s^2C^*L_1||L_2\right]$$

Again this is valid only if both  $L_1 \xrightarrow{\circ} L_2$  and  $L_1/R > RC$  $\begin{bmatrix} 1 + s \frac{L_1}{R} \end{bmatrix} \begin{bmatrix} 1 + sRC + s^2 CL_1 \end{bmatrix}$   $\downarrow$ 

> If also we find low Q for the quadratic then we get two well separated roots from the quadratic term as well

c. First and Second Terms Have Close Roots This is a case when the first inequality is violated.

The inequalities are the same as  $L_1>L_2$  and  $RC>L_2/R$ . It is no longer required that  $L_1/R >>RC$ 

Return to Quadratic  $(1+s\frac{L_2}{R})$  form for first and second roots  $s_1$  and  $s_2$ . We still have a valid approx. for  $s_3 = -a_2/a_3 = L_2/R$  and the quadratic is simply

$$(1+s(\frac{L_1+L_2}{R})+s^2L_1C) = (1+sL_1/R + s^2L_1C)$$

Solve this quadratic for  $s_1$  and  $s_2$ 

### 4. Fourth Order Polynomial from a two section L-C Filter

Erickson Pbm. 8.19.

a. Two Section L-C Filter

A two-section L-C filter has the following transfer function:

$$G(s) = \frac{1}{1 + s\left(\frac{L_1 + L_2}{R}\right) + s^2(L_1(C_1 + C_2)) + s^3\left(\frac{L_1L_2C_1}{R}\right) + s_4(l_1L_2C_1C_2)}$$
  
The element values are:

The element values are:

 $\begin{array}{ccc} R & 50 \text{ mW} \\ C_1 & 680 \text{ mF} & C_2 & 4.7 \text{ mF} \end{array}$ 

$$\approx \frac{1}{(1 + \frac{s}{w_0})(1 + \frac{s}{Qw_1} + \frac{s^2}{w_1^2}) + (1 + \frac{s}{w_2})}$$

Compare coefficients, we get

$$w_{0} = \frac{1}{0.011} = 90.9 \text{ rad/s} \Rightarrow f_{0} = \underline{14.47 \text{ Hz}}$$

$$w_{1} = \sqrt{\frac{a_{1}}{a_{3}}} = 5687.96 \text{ rad/s} \Rightarrow f_{1} = \underline{905.3 \text{ Hz}}$$

$$Q w_{1} = \frac{a_{1}}{a_{2}} \Rightarrow Q = \underline{5.65 \text{ db}}$$

$$w_{2} = \frac{a_{3}}{a_{4}} = 4255319 \text{ rad/s} \Rightarrow f_{2} = \underline{677.3 \text{ KHz}}$$

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Justification:

Approx. exp.

$$\begin{array}{rcl} G(s) &=& \displaystyle \frac{1}{1 + (\frac{a_2}{a_1} + a_1 + \frac{a_4}{a_3}) \, s + (a_3 + a_2 + \frac{a_2 \, a_4}{a_1 a_3}) \, s^2 + (a_3 + \frac{a_4}{a_3} + \frac{a_4 \, a_2}{a_4 \, s^2})} \\ & \downarrow & \downarrow & \downarrow & \downarrow & \downarrow & \downarrow \\ A_o & A_1 & A_2 & A_3 & A_4 \end{array}$$

Numerical values:

	Pole Finder	
	Actual	Factoring Approx.
Ao	1	1
A <sub>1</sub>	1.1 x 10 <sup>-2</sup>	1.103 x 10 <sup>-2</sup>
A <sub>2</sub>	3.43 x 10 <sup>-7</sup>	3.76 x 10 <sup>-7</sup>
A <sub>3</sub>	3.4 x 10 <sup>-10</sup>	3.4008 x 10 <sup>-10</sup>
A <sub>4</sub>	7.99 x 10 <sup>-17</sup>	7.99 x 10 <sup>-17</sup>



We wish to reduce Q from 5.65 db to 2 db <u>without changing</u> break frequencies. Is this possible?

(c) Now Q = 2 From 1, 2  $\rightarrow \frac{a_2}{a_1} = \frac{1}{Qw_1}, \frac{a_3}{a_1} = \frac{1}{w_1^2} - w_1 = \sqrt{\frac{a_1}{a_3}}$   $\Rightarrow Q \sqrt{\frac{a_1}{a_3}} = \frac{a_1}{a_2} = \frac{\sqrt{a_1a_3}}{a_2}$ but C<sub>1</sub> >> C<sub>2</sub>  $\Rightarrow$ L<sub>1</sub> >> L<sub>2</sub>  $\Rightarrow$  approx.  $a_1 \approx \frac{L_1}{R}$   $a_2 \approx L_1C_1$   $a_3 \approx \frac{L_1L_2C_1}{R}$  $a_4 \approx L_1L_2C_1C_2$ 

Then 
$$Q = \frac{\sqrt{\frac{L_1}{R} \frac{L_1 L_2 C_1}{R}}}{L_1 C_1} = \frac{1}{R} \sqrt{\frac{L_2}{C_1}}$$
$$w_0 = \frac{1}{a_1} = \frac{R}{L_1}$$
$$w_1 = \sqrt{\frac{a_1}{a_3}} = \frac{1}{\sqrt{L_2 C_1}}$$
$$w_2 = \frac{a_3}{a_4} = \frac{1}{RC_2}$$
So Q depends only on R, L<sub>2</sub>, C<sub>1</sub>. Let's change L<sub>2</sub> and C<sub>1</sub>. Can't change R as it changes w<sub>0</sub>  
R = 50 mW, Q = 2  
 $\Rightarrow L_2 = 0.01 C_1 \text{ and } C_1 = \frac{10}{w_1} = \frac{10}{5688} = 1.76 \text{ mF}$   
L<sub>2</sub> = 0.01 C<sub>1</sub> = 17.581 mH  
Check:

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$$w_1 = \sqrt{\frac{a_1}{a_3}} = \sqrt{\frac{L_1 + L_2}{L_1 L_2 C_1}} = 5687.96 \text{ rad/s}$$
  
$$\Rightarrow f = 905.3 \text{ Hz} \Rightarrow \text{same}$$

## B. Buck-Boost $T_{vd}(s)$ and $T_{vg}(s)$ Analysis

This is the crucial material for this lecture as part A can be replaced by using a root solver on a basic calculator.

1. Equivalent ac Small Signal model The Buck-Boost small signal circuit model was developed earlier.



#### The buck-boost small signal equations follow.

Small-signal ac equations of the buck-boost converter, derived in section 7.2:

$$L \frac{d\hat{i}(t)}{dt} = D\hat{v}_g(t) + D'\hat{v}(t) + \left(V_g - V\right)\hat{d}(t)$$
$$C \frac{d\hat{v}(t)}{dt} = -D'\hat{i}(t) - \frac{\hat{v}(t)}{R} + I\hat{d}(t)$$
$$\hat{i}_g(t) = D\hat{i}(t) + I\hat{d}(t)$$

## We aim to develop these equations to yield **two separate** transfer functions as described below $G_{vg}$ and $G_{vd}$ :

The converter contains two inputs,  $\hat{d}(s)$  and  $\hat{v}_{g}(s)$  and one output,  $\hat{v}(s)$ 

Hence, the ac output voltage variations can be expressed as the superposition of terms arising from the two inputs:

$$\hat{v}(s) = G_{vd}(s) \ \hat{d}(s) + G_{vg}(s) \ \hat{v}_g(s)$$

The control-to-output and line-to-output transfer functions can be defined as

$$G_{vd}(s) = \frac{\hat{v}(s)}{\hat{d}(s)} \bigg|_{\hat{v}_{g}(s) = 0} \text{ and } G_{vg}(s) = \frac{\hat{v}(s)}{\hat{v}_{g}(s)} \bigg|_{\hat{d}(s) = 0}$$

This is our goal to extract these two functions for the buck-boost from the equivalent circuit. Setting one input to zero will result in great simplifications to the loop equations of the full circuit.

2. Loop Equations:  $\hat{i}_{g}(t) = D\hat{i}(t) + I\hat{d}(t)$  (for the first circuit loop on left above)  $L\frac{d\hat{i}}{dt} = D_{Vg}(t) + D'v(t) + (v_{g}-v)\hat{d}$  (middle circuit loop) 
$$\hat{\mathbf{d}}(\mathbf{s}) \begin{vmatrix} \mathbf{I} \\ \hat{\mathbf{v}}g(\mathbf{s})=\mathbf{0} \end{vmatrix} \hat{\mathbf{v}}_g(\mathbf{s}) \begin{vmatrix} \mathbf{I} \\ \hat{\mathbf{d}}(\mathbf{s})=\mathbf{0} \end{vmatrix}$$

Simple but tedious algebra leads to:

$$sC\hat{v}(s) = -\frac{D'}{sL} \left( D\hat{v}_{g}(s) + D'\hat{v}(s) + \left( V_{g} - V \right) \hat{d}(s) \right) - \frac{\hat{v}(s)}{R} + I\hat{d}(s)$$
$$\hat{v}(s) = \frac{-DD'}{D'^{2} + s\frac{L}{R} + s^{2}LC} \hat{v}_{g}(s) - \frac{V_{g} - V - sLI}{D'^{2} + s\frac{L}{R} + s^{2}LC} \hat{d}(s)$$

write in normalized form:

$$\hat{\mathbf{v}}(s) = \left(-\frac{D}{D'}\right) \frac{1}{1+s\frac{L}{D'^2R} + s^2\frac{LC}{D'^2}} \hat{\mathbf{v}}_g(s) - \left(\frac{V_g - V}{D'^2}\right) \frac{\left(1-s\frac{LI}{V_g - V}\right)}{1+s\frac{L}{D'^2R} + s^2\frac{LC}{D'^2}} \hat{d}(s)$$

Or in the form, that we will use later, after putting DC analysis in.  $V_{\sigma} - V - sLI \quad \gamma \qquad DD'\hat{v}_{\sigma}(s)$ 

## 3. Line to Output Transfer Function

Hence, the line-to-output transfer function is

$$G_{vg}(s) = \frac{\hat{v}(s)}{\hat{v}_{g}(s)} \bigg|_{\hat{d}(s)=0} = \left(-\frac{D}{D'}\right) \frac{1}{1+s\frac{L}{D'^{2}R} + s^{2}\frac{LC}{D'^{2}}}$$

which is of the following standard form:

$$G_{vg}(s) = G_{g0} \frac{1}{1 + \frac{s}{Q\omega_0} + \left(\frac{s}{\omega_0}\right)^2}$$

In standard form, such that coefficients of s<sup>o</sup> are unity,  $\Rightarrow$ The line (mains) to output transfer function is:

 $T_{vg}(s) = \left(\frac{-D}{D'}\right) \frac{1}{1 + s\frac{L}{D'^2R} + s^2 \frac{LC}{D'^2}}$  Pole location in the dynamic transfer

function depend on the **choice of DC operating point, D**.  $L_{eff}(D) \equiv L/(D')^2 = f(D, L, C, R).$ 

In standard quadratic pole pair form:  $T_{vg}(s) = \frac{T_{go} \text{ (constant low f gain)}}{1 + \frac{s}{Qw_o} + (\frac{s}{w_o})^2} \text{ with Q in db units}$ 

Identify like terms

$$\begin{array}{ll} T_{go} &\equiv -\frac{D}{D'} & \mbox{Buck-boost Gain @ dc} \\ w_o &\equiv \frac{1}{\sqrt{L_{eff}C}} &= \frac{D'}{\sqrt{LC}} & \mbox{The dynamic transfer function rollover} \\ \mbox{frequency depends on the choice of DC operating point f(D')} \\ \frac{1}{Qw_o} &= \frac{L_{eff}}{R} \\ Q &= D'R \sqrt{\frac{c}{L}} & \mbox{Note also that the second order Q also depends on} \end{array}$$

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the DC operating point chosen for the converter Q = f(D')

4. Control to Output Transfer Function:  $T_{vd}(s) \equiv \frac{\hat{v}(s)}{\hat{d}(s)} \Big|_{\hat{v}_s(s) = 0}$ 

$$T_{vd}(s) = f(D', V_g, V, I, L, C, R)$$

All DC voltages/currents as well as D and component values of L and C effect the dynamic response.

In standard form such that s<sup>o</sup> coefficients are unity:

$$T_{vd}(s) = \left[-\frac{Vg - V}{D'^2}\right] \frac{1 - s\frac{LI}{Vg - V}}{1 + s\frac{Leff}{R} + s^2LeffC}$$

or in the standard form below,

$$G_{vd}(s) = \frac{\hat{v}(s)}{\hat{d}(s)}\Big|_{\hat{v}_{g}(s) = 0} = \left(-\frac{V_{g} - V}{D'^{2}}\right) \frac{\left(1 - s \frac{LI}{V_{g} - V}\right)}{\left(1 + s \frac{L}{D'^{2} R} + s^{2} \frac{LC}{D'^{2}}\right)}$$

Standard form:

$$G_{vd}(s) = G_{d0} \frac{\left(1 - \frac{s}{\omega_z}\right)}{\left(1 + \frac{s}{Q\omega_0} + \left(\frac{s}{\omega_0}\right)^2\right)}$$

Poles of  $T_{vd}(s)$  are the same as poles of  $T_{vg}(s)$ , but the zero is unique. **It is a right half plane zero**. We effect  $w_z$  location by choice of all DC values for  $V_g$ , V, I etc.  $w_z(RHP ZERO) = D'R/DL$ we will show below. Compare to standard. quadratic form:  $T_{do}(constant low f gain)[1 - s/w_z] = T_{VD}(s)$ 

$$1 + \frac{s}{Qw_o} + (\frac{s}{w_o})^2$$

It's easy to design the DC gain for Buck-Boost given V(out) and

the duty cycle.

$$T_{do}(dc) = \frac{V_g - V}{D'^2} = -\frac{V_g}{D'^3} = \frac{V}{DD'^2}$$

Recall for Buck-Boost DC conditions:  $\frac{V}{V_g} = -\frac{D}{D'}$  and  $I = -\frac{V}{R} \frac{1}{D'}$ 

Be sure to note that I(DC) is in inner loop of the buck-boost circuit model on page 12. Then we find from the DC conditions the zero location:

$$w_{Z} = \frac{V_{g} - V}{LI} = \frac{D'}{D} \frac{R}{L}$$
 right-half plane  

$$G_{A0} = -\frac{V_{g} - V}{D'^{2}} = -\frac{V_{g}}{D'^{3}} = \frac{V}{DD'^{2}}$$

$$\omega_{z} = \frac{V_{g} - V}{LI} = \frac{D'}{DL} \qquad (RHP)$$

$$\omega_{0} = \frac{D'}{\sqrt{LC}}$$

$$Q = D'R \sqrt{\frac{C}{L}}$$

— Simplified using the dc relations:  $V = -\frac{D}{D'}V_g$ 

$$V = -\frac{D}{D'} V_{L}$$
$$I = -\frac{V}{D'R}$$

Next we try to get more specific and quantitative.

## 5. Specific Buck-Boost Design

### a. We know DC operating conditions

$$V = -\frac{.6}{.4} \ 30 = -45 V$$

 $I = \frac{-45}{10} \frac{1}{.4} = 11.25 A$  in middle loop of model, on page 12.

We also find  $I_{out} = -4.5A$  in the outer loop of the page 12 circuit. Recall that  $I(middle) = I_{out} + I_{in}$ ? for CCM conditions

**b.** AC values in std. transfer functions (using db for plots) for these DC conditions.

First a  $T_{VG}(s)$  having only poles is considered.

$$G_{go} = \frac{D}{D'} = 1.5 \text{ or } 3.5 \text{ in db units}$$

$$f_o = \frac{W_o}{2p} = \frac{D'}{2p\sqrt{LC}} = 400 \text{ Hz}$$

$$Q = D'R \sqrt{\frac{C}{L}} = 4 \text{ in normal units or } 12 \text{ in db units}$$

$$\frac{T_{vg}(s) = -\frac{D}{D'} = 1.5 = 3.5 \text{db}}{1 + \frac{s}{(12)(2pf_o)}} + (\frac{s}{2pf_o})^2 + \frac{1}{\sqrt{Q}} + \frac{1}{\sqrt$$

Now for a  $T_{\text{VD}}(s)$  having a right hand plane zero we have a new set of values.

$$G_{do} = \frac{V}{D(D')^2} = 469 \text{ or } 53.4 \text{ db}$$

$$f_z = \frac{D'R}{2pDL} = 6.6 \text{ kHz}$$

$$\frac{T_{vd}(s) = G_{do}\left(1 - \frac{s}{w_z}\right)}{1 + \frac{s}{(12)2p}f_o} + \left(\frac{s}{2p}f_o\right)^2}$$

$$\downarrow \qquad \qquad \downarrow$$

### Q in db 400 Hz

#### In summary:

Suppose we are given the following numerical values:

Then the salient features have the following numerical values:

$$D = 0.6$$
  

$$R = 10\Omega$$
  

$$V_g = 30V$$
  

$$L = 160\mu H$$
  

$$C = 160\mu F$$

$$G_{g0} = \frac{D}{D'} = 1.5 \Rightarrow 3.5 \text{dB}$$

$$G_{d0} = \frac{|V|}{D D'^2} = 469 \text{V} \Rightarrow 53.4 \text{dBV}$$

$$f_0 = \frac{\omega_0}{2\pi} = \frac{D'}{2\pi\sqrt{LC}} = 400 \text{Hz}$$

$$Q = D'R \sqrt{\frac{C}{L}} = 4 \Rightarrow 12 \text{dB}$$

$$f_z = \frac{\omega_z}{2\pi} = \frac{D'R}{2\pi DL} = 6.6 \text{kHz}$$

If L goes to small values(DCM) what happens to the zero location? In  $G_{vd}(f)$  **Phase** Plots we will find

Two poles cover  $180^{\circ}$  over a frequency range, where now Q is in real units and not in db

 $f_{lower} = 10^{-\frac{1}{2Q}} f_o = 10^{-1/8} (400) = 300 \text{ Hz}$  $f_{upper} = 10^{+\frac{1}{2Q}} f_o = 10^{1/8} 400 = 533 \text{ Hz}$ 

 $In G_{vd}(f)$  **Gain** Plots we will find: We have a double pole at 400 Hz showing a resonant "Q peak".



The RHP zero will add amplitude above  $f_z = 6.6$  kHz but will cause additional phase lag threatening stability if we use feedback.

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## 20 Below are plotted [Gvd] and **Đ**Gvd

Note carefully the unity gain line at "0" db.



RH plane zero starts to contribute phase angle at 660 Hz and LIMITS phase margin at unity gain. Additional phase contribution starts at  $f_z/10$  @ 660 Hz.

The  $G_{vg}$  amplitude and phase is much simpler as shown below.



Here the f margin at unity gain is also small but not as bad as  $T_{vd}$ .

# C. Right Half Plane Zero Issues and Indicators of This $\langle i_{\scriptscriptstyle D} \rangle_{\tau_s} = d^* \langle i_{\scriptscriptstyle L} \rangle_{\tau_s}$



The above two switch mode circuits have right-half plans zeros. **1. Transient Response Indicators of a RHP zer**o



This RH plane zero occurs for several PWM dc-dc converters boost buck-boost flyback It does not occur for buck converter. WHY?

### 2. Origin of RH Plane Zero

\*

Differing time delays for D' which changes V<sub>o</sub> immediately & i<sub>m</sub> which changes V<sub>o</sub> only slowly due to switching over several switch cycles, causing initial V<sub>o</sub> to fall for all converters with output driven by i<sub>m</sub>. We have therefore the case that: (i<sub>m</sub>) stays the same over short  $\Delta t$  time periods but D and D' change fast. D chnages instantaneously by the control circuit knob, this instantaneously lower D' and thus V<sub>o</sub> goes down initially and only later increases when the effect of bigger D makes it to the output.

 $i_{Lm}$  <u>cannot</u> change instantaneously time interval d' = 1-d can change instantaneously which is the duration  $i_m$  is delivered to the output. This causes a phase lag and momentary drop in V<sub>o</sub> followed by subsequent increase in V<sub>o</sub> sudden change in d that builds up  $i_m$  slowly.

 $i_L$  varies slowly  $i_D$  duration changes instantaneously but  $i_{Lm}$  does not  $\;\Rightarrow V_o\downarrow$  then  $\uparrow$ 





## 4. Phase Margin Limitations of Any Closed Loop with RHP Zero Present

 $A_{CL} = \frac{A_{oL}}{1 + A \mathbf{b}}$  Want  $(A\beta) = 1$ 

Then < A $\beta$  far from 180°

How far?  $76^{\circ}$  away (for conservative designer),  $60^{\circ}$  for average designer and  $10^{\circ}$  away for poor designer!

Flyback converter bode plots:

Oddity is gain (low f) is non-linear w.r.t.  $V_d$ f range with -40db/decade drop depends on low f gain. Not enough  $\phi$  margin come close to instability at unity gain  $\phi \rightarrow 180^{\circ}$ 



Note the unity gain location at "0" db.

Flyback operating DCM does not have right half plane zero! Without LH plane zero above the phase margin would be nil. Moreover, RH plane zero returns phase to 180°.