



Intern position for EE Digital Hardware Designer at Verigy in Ft Collins

The successful applicant will be assisting in the design of test equipment hardware under the direction of our engineering staff. The expectation is that this individual will be able to develop an FPGA hardware design from a project requirements document, test that hardware design using simulation techniques, and then turn that design into physical reality. This person must be located in Ft Collins, CO and will work up to 20 hours per week.

Requirements:

- Junior or above student in electrical engineering
- Digital hardware design knowledge
- Knowledge of hardware description language (we use Verilog)
- c/c++ programming skills
- Must be authorized to work in the US full time for any employer.

Desired:

- GPA of 3.5 or higher
- Grad student

Verigy designs, manufactures, sells and services advanced test systems and solutions for the semiconductor industry. Our legacy of innovation began with Hewlett-Packard and continued as Agilent Technologies. Now the world's leading semiconductor companies look to Verigy for innovative test solutions that make their innovations possible. We help them deliver with enabling and future-ready test solutions.

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Keywords: 129 or Ft Collins

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