

Sample Plan of Study: MS Plan B VLSI Area

Rev. 3 August 2006

Semester 1

EE450/451	Digital System Design and Laboratory	(4)
EE554	Computer Architecture	(3)
ST520	Intro to Probability Theory	(4)

Semester 2

EE571/EE575	VLSI Design	(4)
EE520	Optimization Theory	(3)
CS414	Object Oriented Design	(4)

Semester 3

EE660	Adv. Topics in VLSI Design	(3)
M517	Math Analysis I	(3)
EE536	RF IC Design	(3)

TOTAL CREDITS (31)

NOTE: This sample plan contains 31 credits of regular coursework. 32 credits are required for M.S. Plan B. Consult the ECE Graduate Student Handbook for complete requirements.