

Sample Plan of Study: MS Plan B Semiconductor Devices and Processing Area

Rev. 3 August 2006

Semester 1

EE404	Experiments in Optical Electronics	(2)	
EE441	Optical Electronics	(3)	
EE471	Semiconductor Devices	(3)	

Semester 2

EE525	Fiber Optic Communications	(3)	Odd Years
EE534/EE535	Analog IC Design and Laboratory	(4)	
EE546	Laser Fundamentals and Devices	(3)	
EE570	Compound Materials and Devices	(3)	Even Years
EE574	Optical Materials and Devices	(3)	Even Years

Semester 3

EE536	RF Integrated Circuit Design	(3)	
EE576	VLSI Processing-Science and Technology	(3)	Odd Years
EE641	Electromagnetics	(3)	Even Years

Semester 4

EE642	Time-Harmonic Electromagnetics	(3)	Odd Years
EE580	Computational Electromagnetics	(3)	Odd Years
PH531	Introductory Solid State Physics	(3)	
EE569	MEMS	(3)	

TOTAL CREDITS (45)

NOTES:

- This sample plan contains 45 credits of regular coursework. Only 32 credits are required for M.S. Plan B. Consult the ECE Graduate Student Handbook for complete requirements.
- A maximum of 6 credit hours of 400 level classes may be applied to an MS degree. Students may choose to take additional 400 level classes to prepare for 500 level classes and above.