

Sample Plan of Study: MS Plan B Embedded Systems Area

Rev. 3 Aug 2010

Semester 1

ECE/CS561	Hardware/Software Design of Embedded Systems	(4)
CS475	Parallel Programming	(4)
ENGR510	Engineering Optimization: Method/Application	(3)

Semester 2

ECE452	Computer Organization and Design	(3)
ECE/CS560	Foundations of Fine-grain Parallelism	(4)

Semester 3

ECE554	Computer Architecture	(3)
ECE670	Heterogeneous Computing	(4)

Semester 4

CS575	Parallel Processing	(4)
ECE661	Advanced Topics in Embedded Systems	(4)

TOTAL CREDITS²⁻³ (33)

Other possible courses

CS453	Introduction to Compiler Construction	(4)
ST520	Introduction to Probability Theory	(3)
CS556	Computer Security	(4)
CS530	Fault-Tolerant Computing	(4)
ECE571/ECE575	VLSI Design ¹	(4)

NOTES:

1. The prerequisites for EE571/EE575 are EE450 and EE451. Incoming students who have not taken equivalent courses need to take EE450 and EE451 in Semester 1.
2. This sample plan contains 33 credits of regular coursework. Only 32 credits are required for MS Plan B. Consult the ECE Graduate Student Handbook for complete requirements.
3. We recommend that students take no more than 2-3 courses each semester, especially in the first semester.