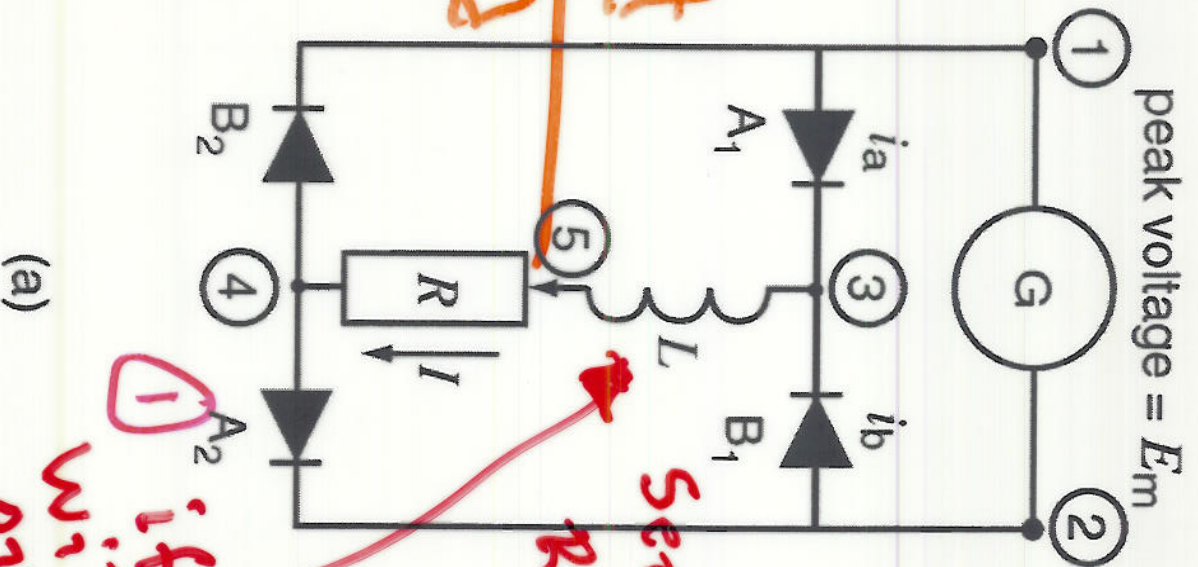


Fig. 2. The waveform for the dc-link voltage on the output of the bridge rectifier illustrates how the holdup capacitance of the filter capacitor is carefully sized to have a specific charge duty cycle, $D_{CH}^{[1]}$.

saturation after several cycles. The choices for reset include

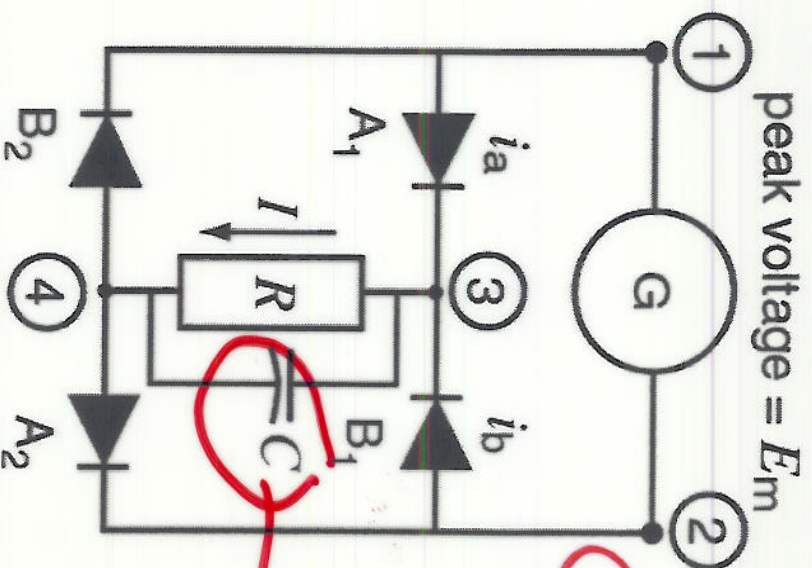
Illegal due to peak is charge C

Fig 21.19 95 1482



① if L "large"
will smooth
out $i_L(t)$

$t_D \approx D$ -wave
Approximation



② Parallel
R-C
will
draw
spikes
 $i = C \frac{dv}{dt}$

DC
 $V_R = 0.9 E_G$

DC vol
 E_R not
affected
by L
 $V_R = ?$

Figure 21-14 a. Rectifier with inductive filter. b. Rectifier with capacitive filter.

